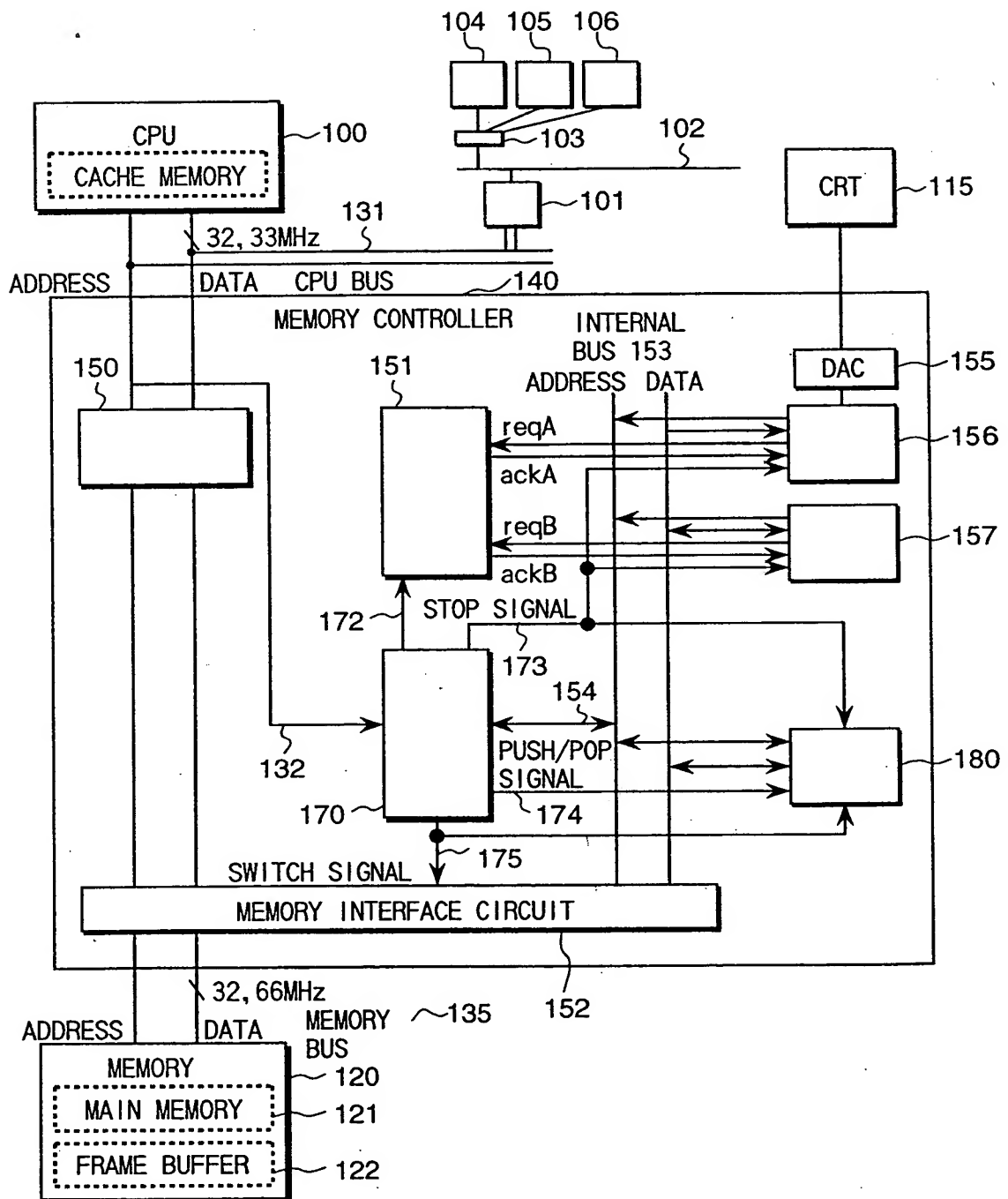


FIG. 1



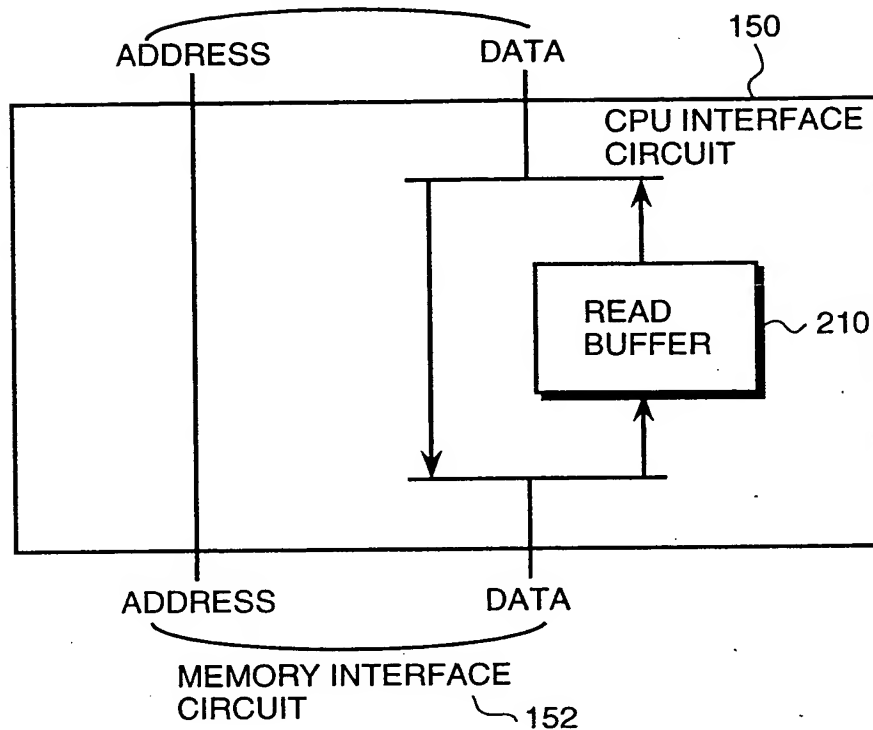
CPU BUS \sim 131

FIG.3

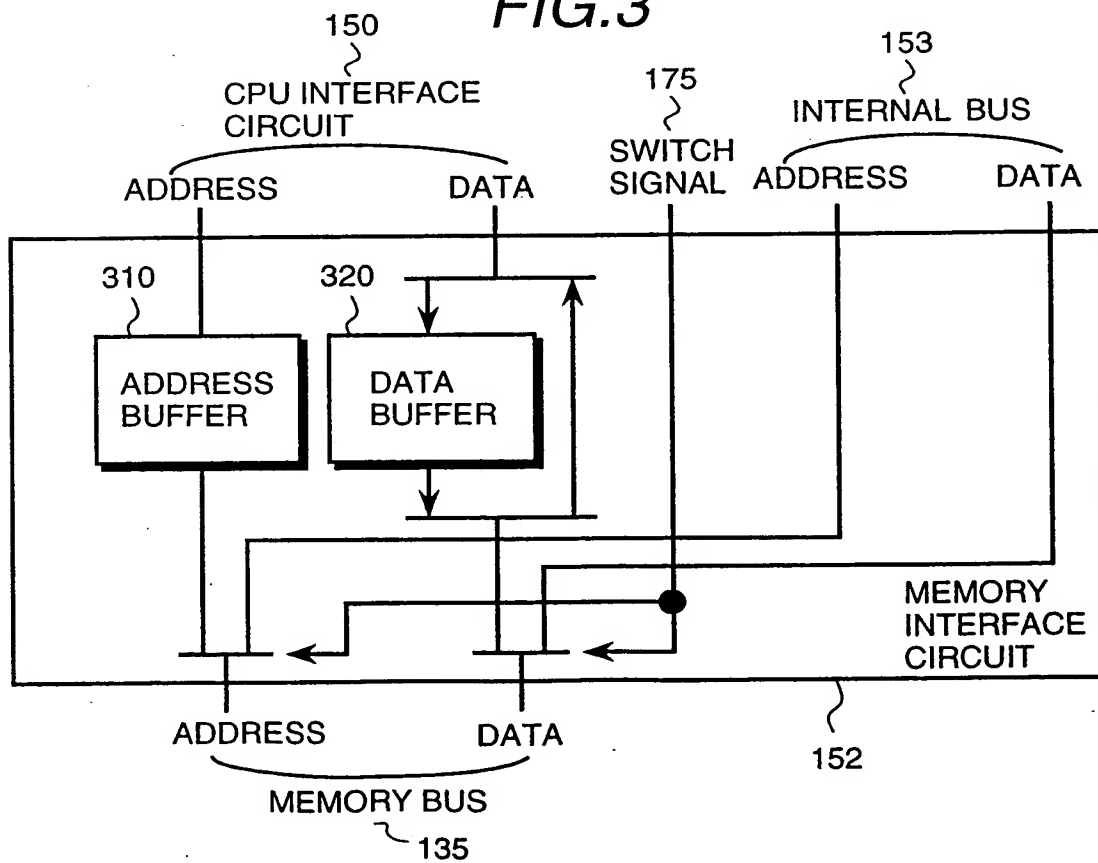


FIG. 4

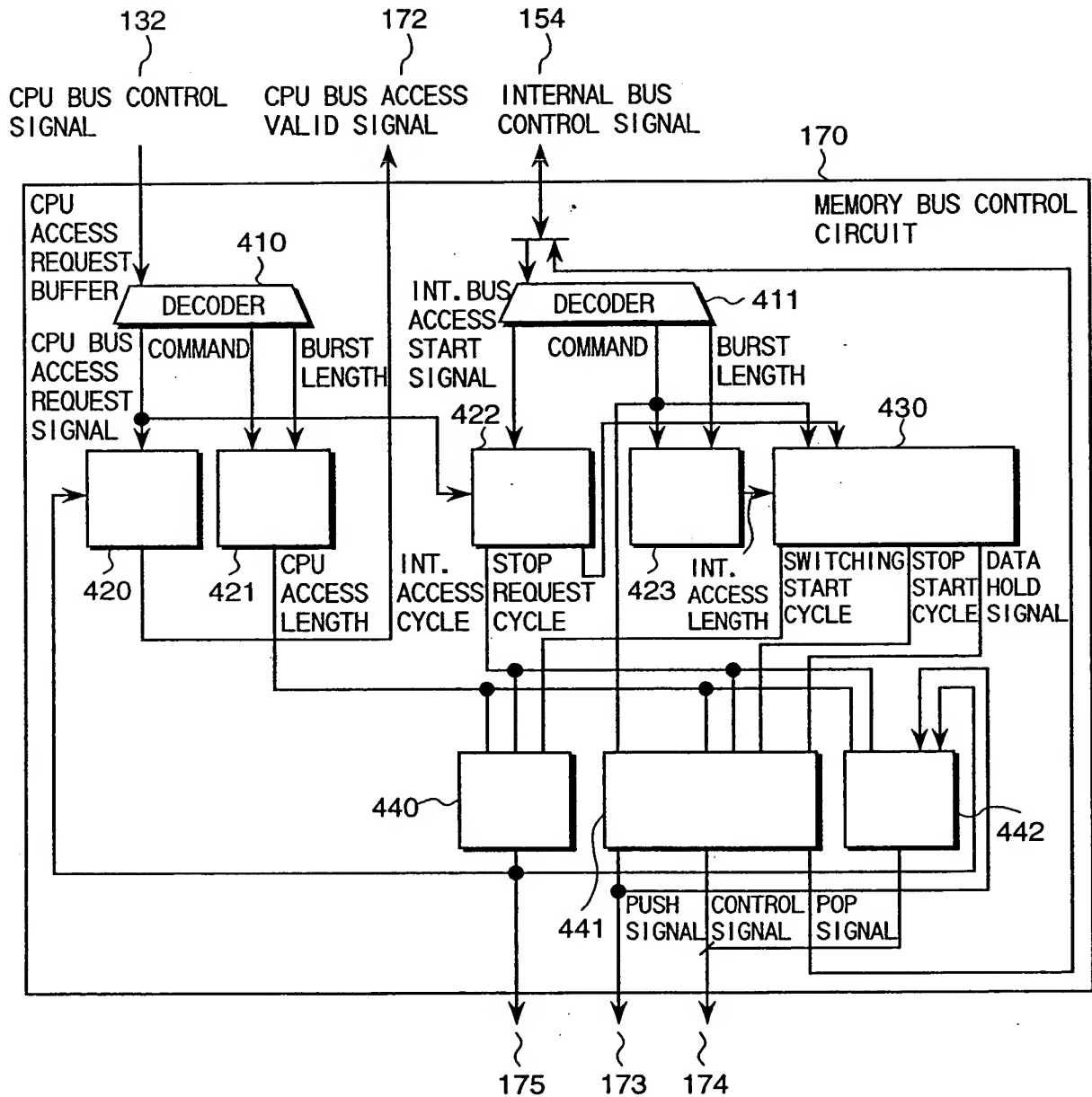


FIG.5

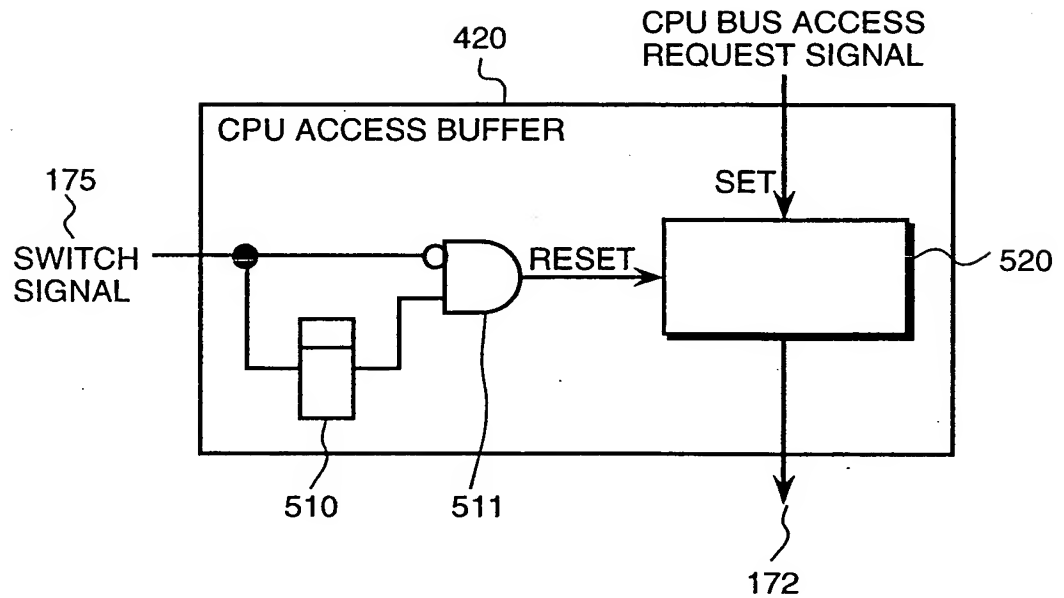


FIG.7

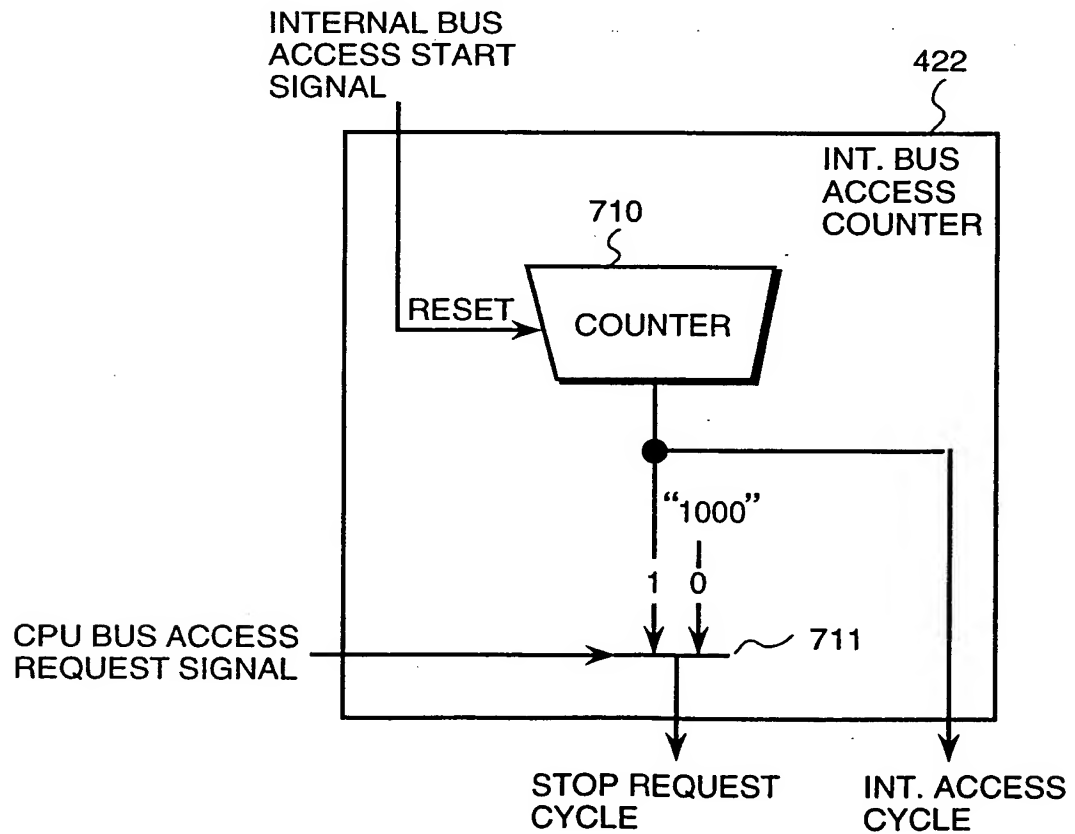


FIG. 6

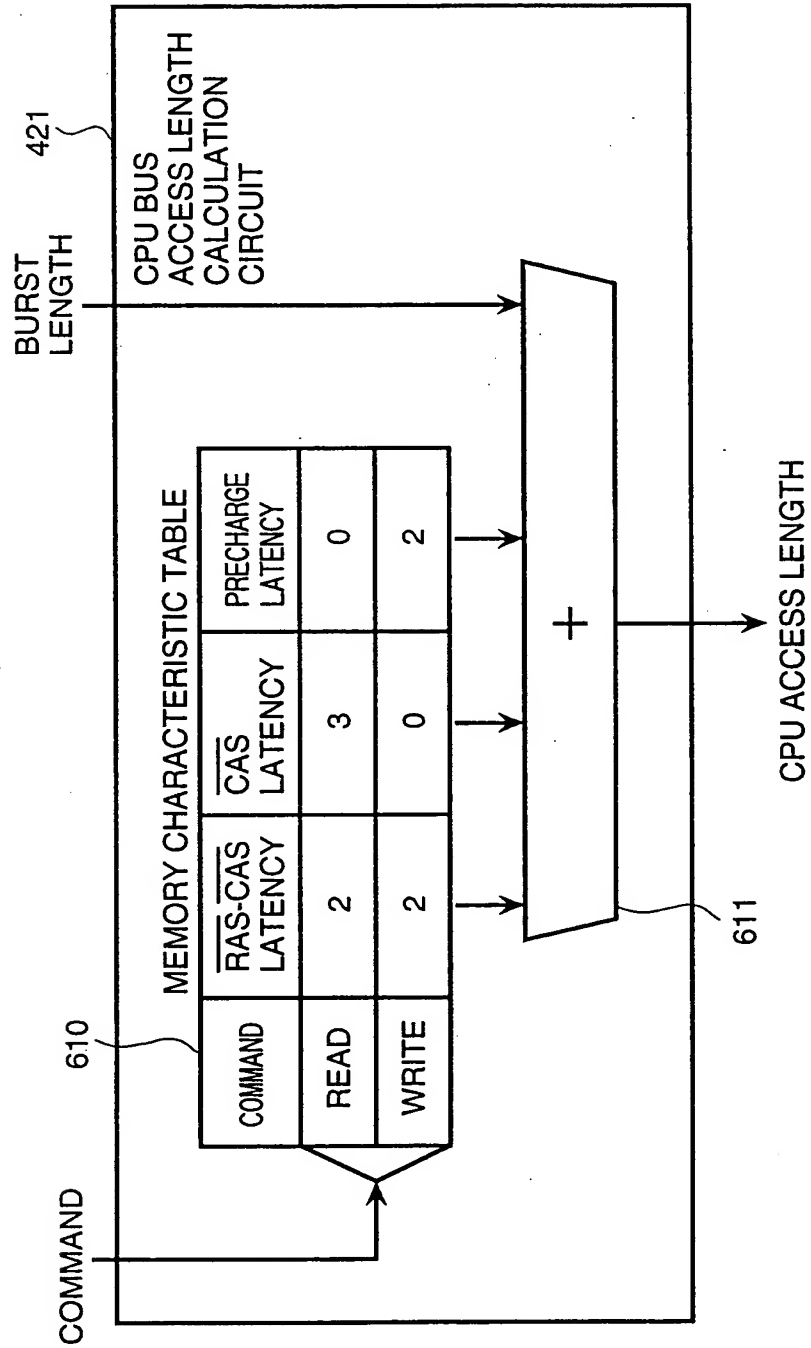


FIG. 8

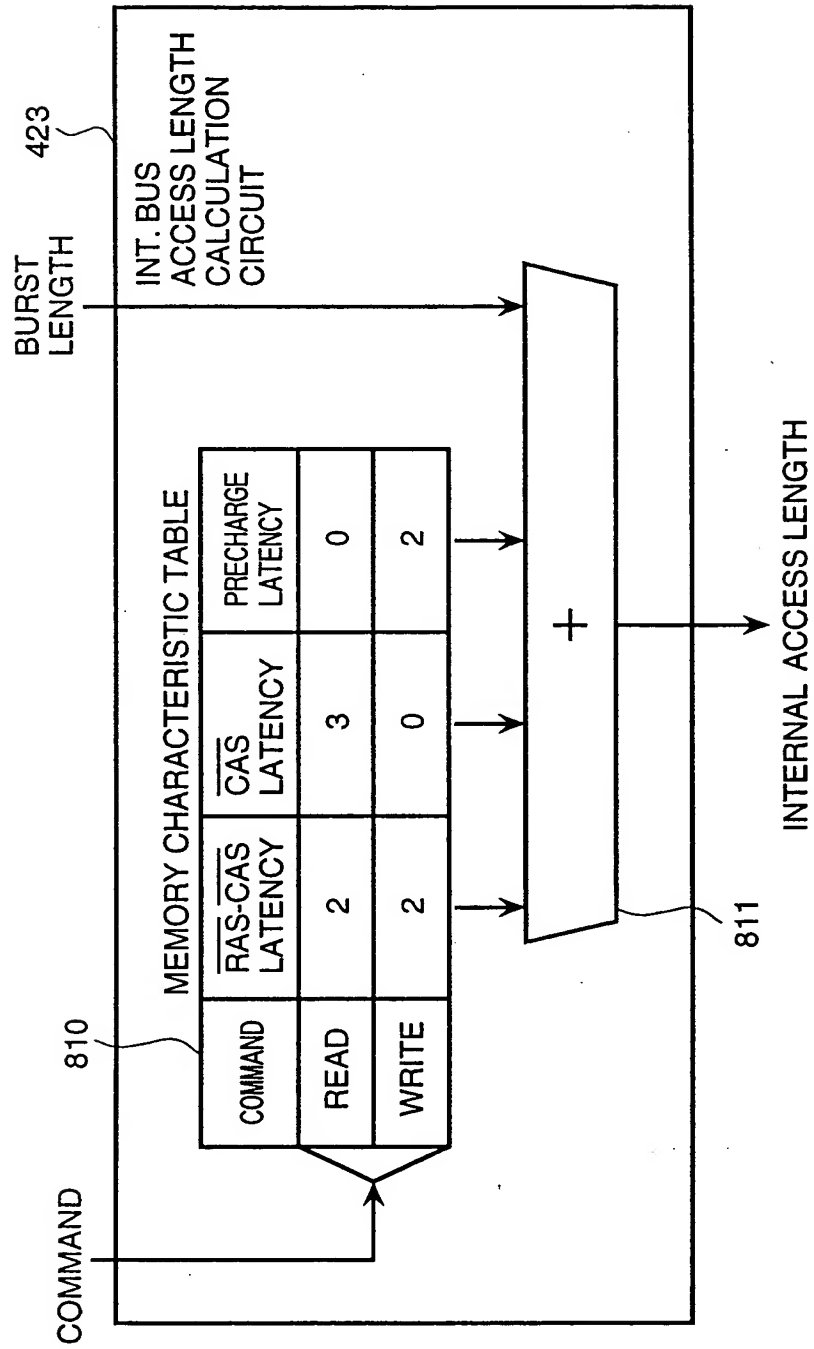


FIG. 9

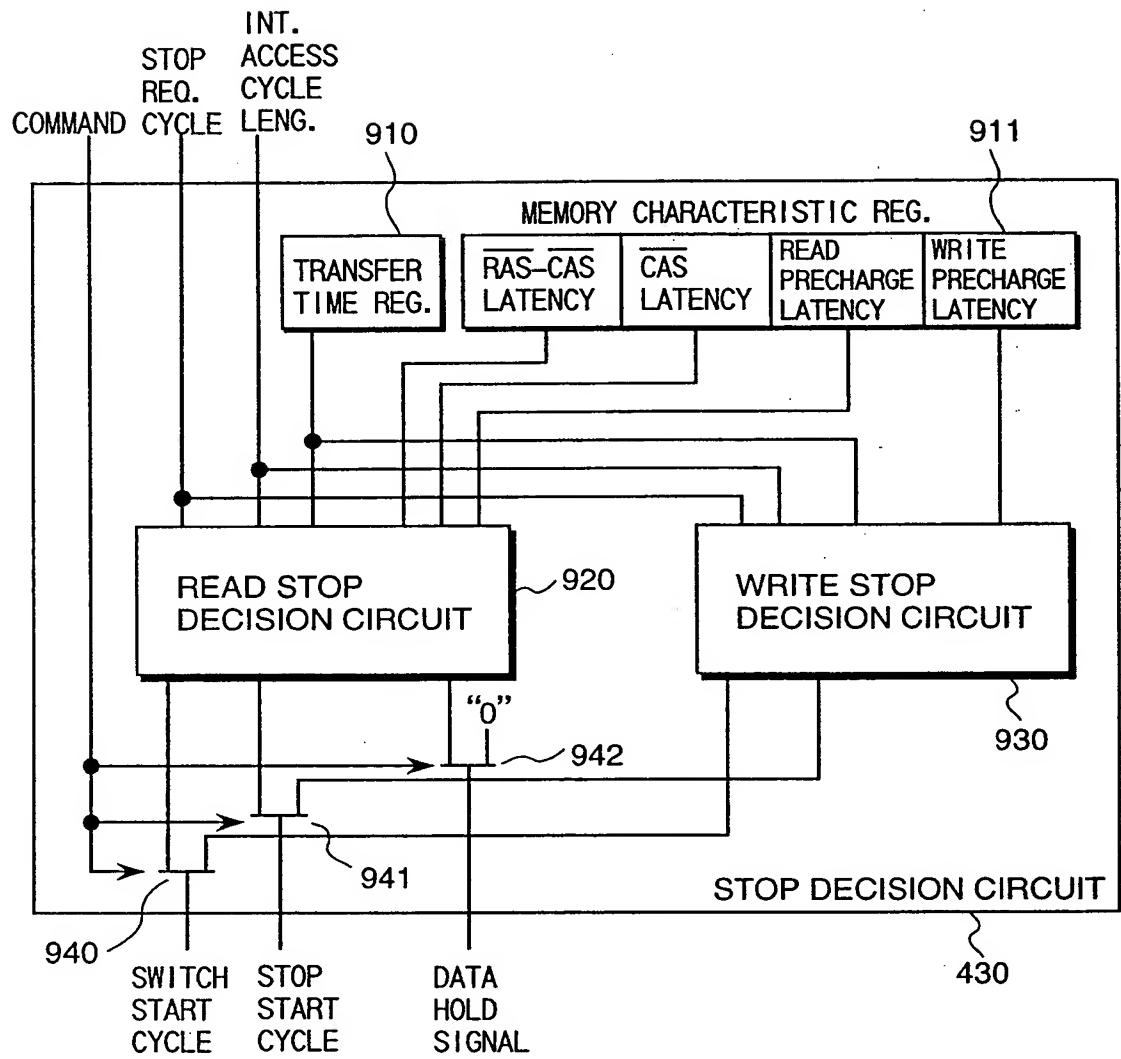


FIG. 10

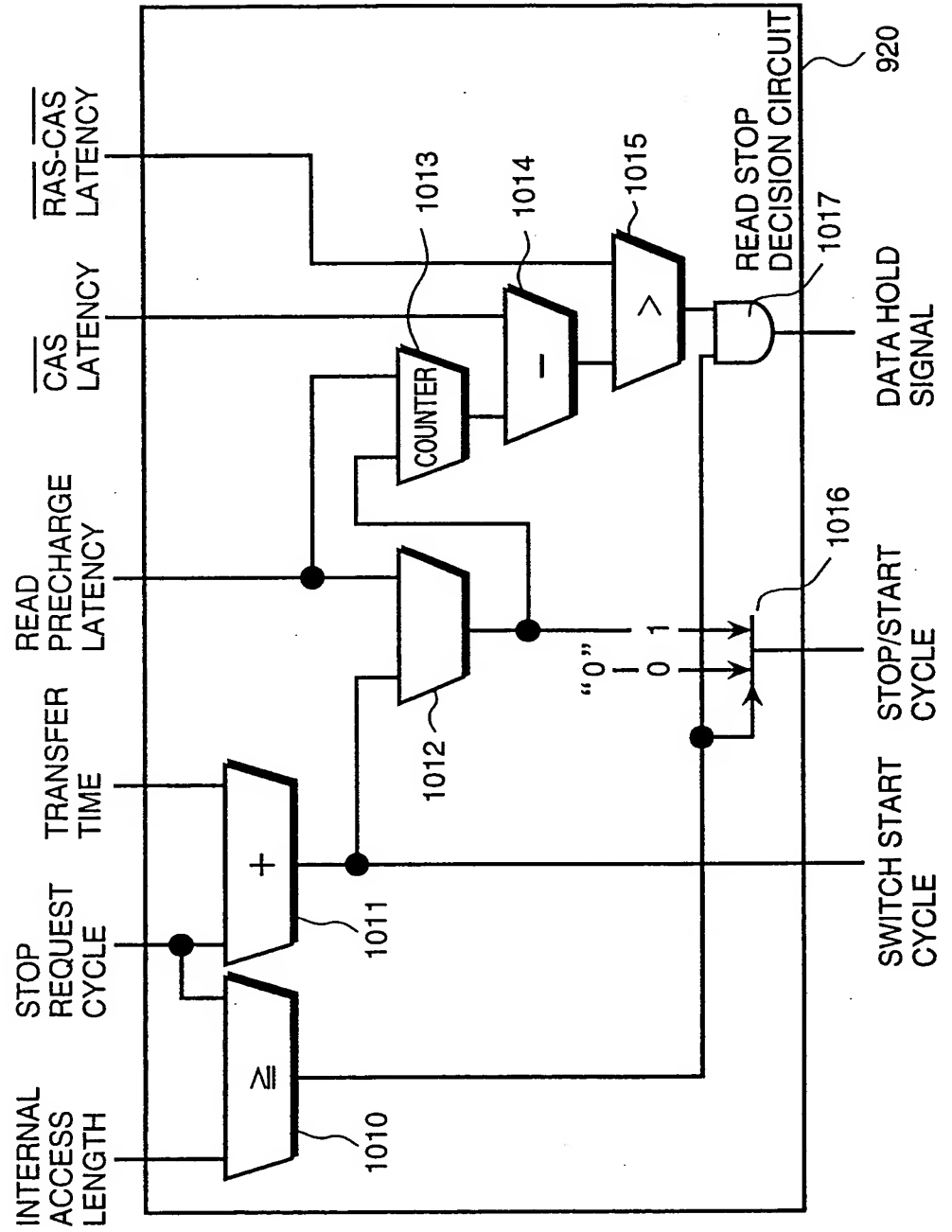


FIG. 11

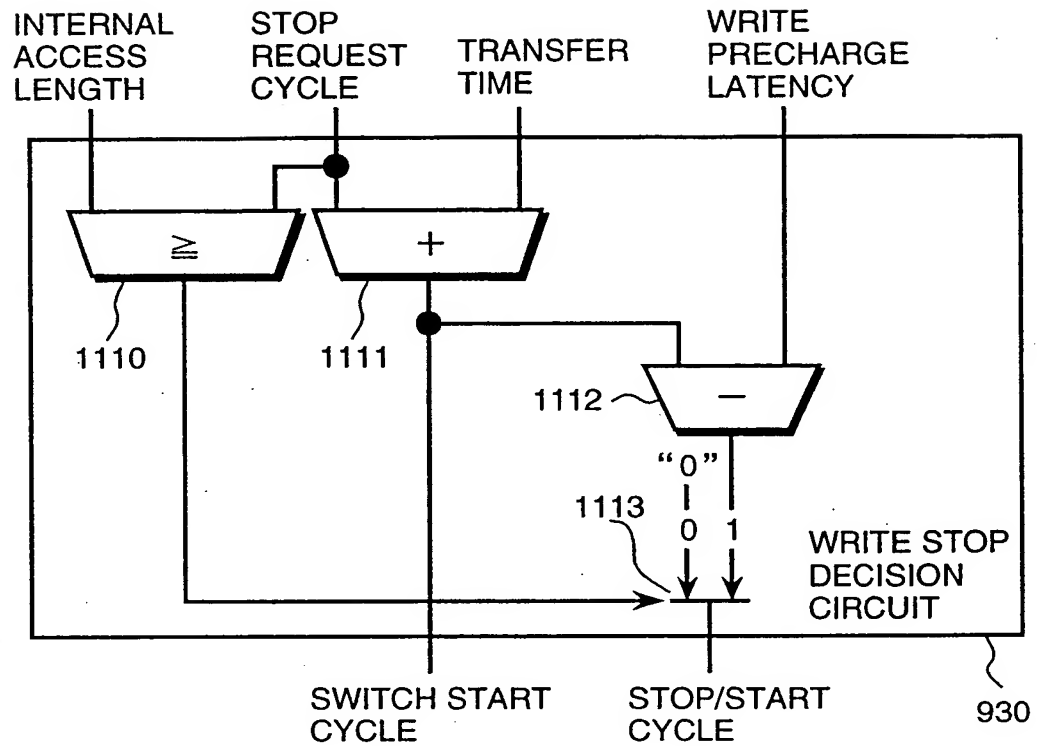


FIG. 12

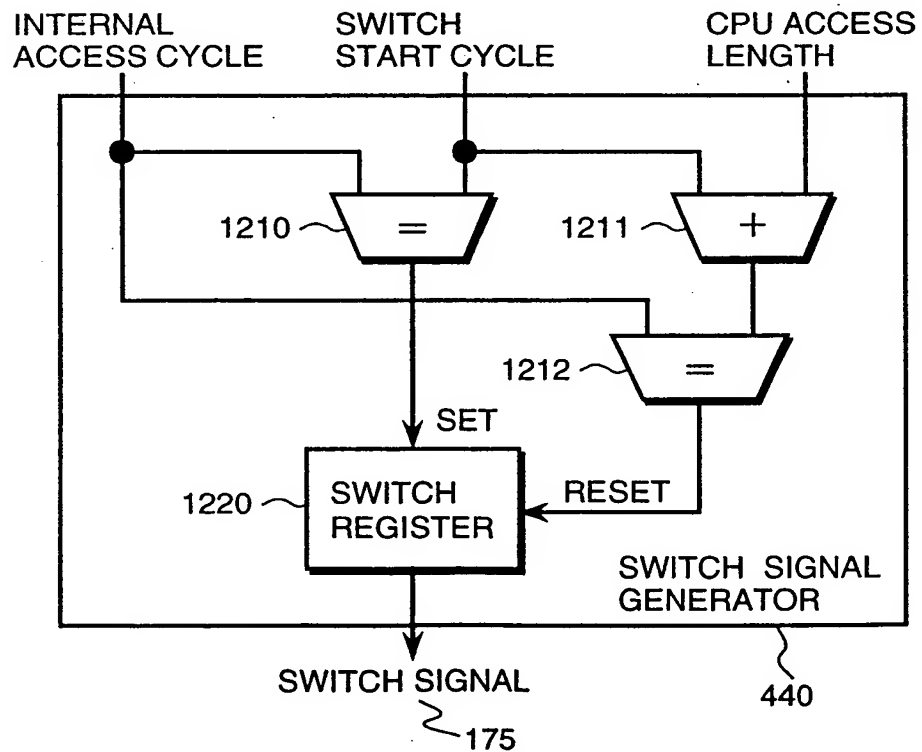


FIG. 13

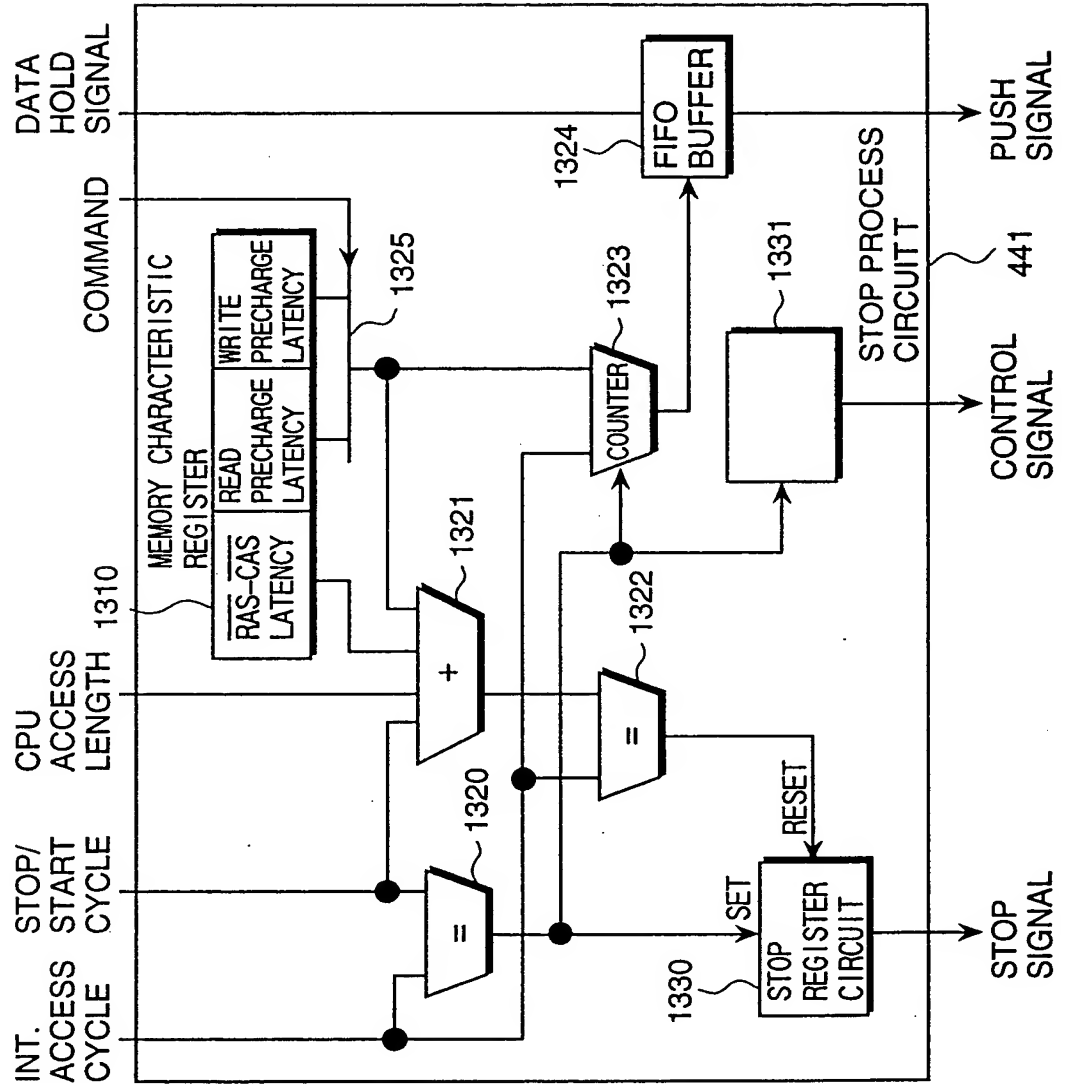


FIG. 14

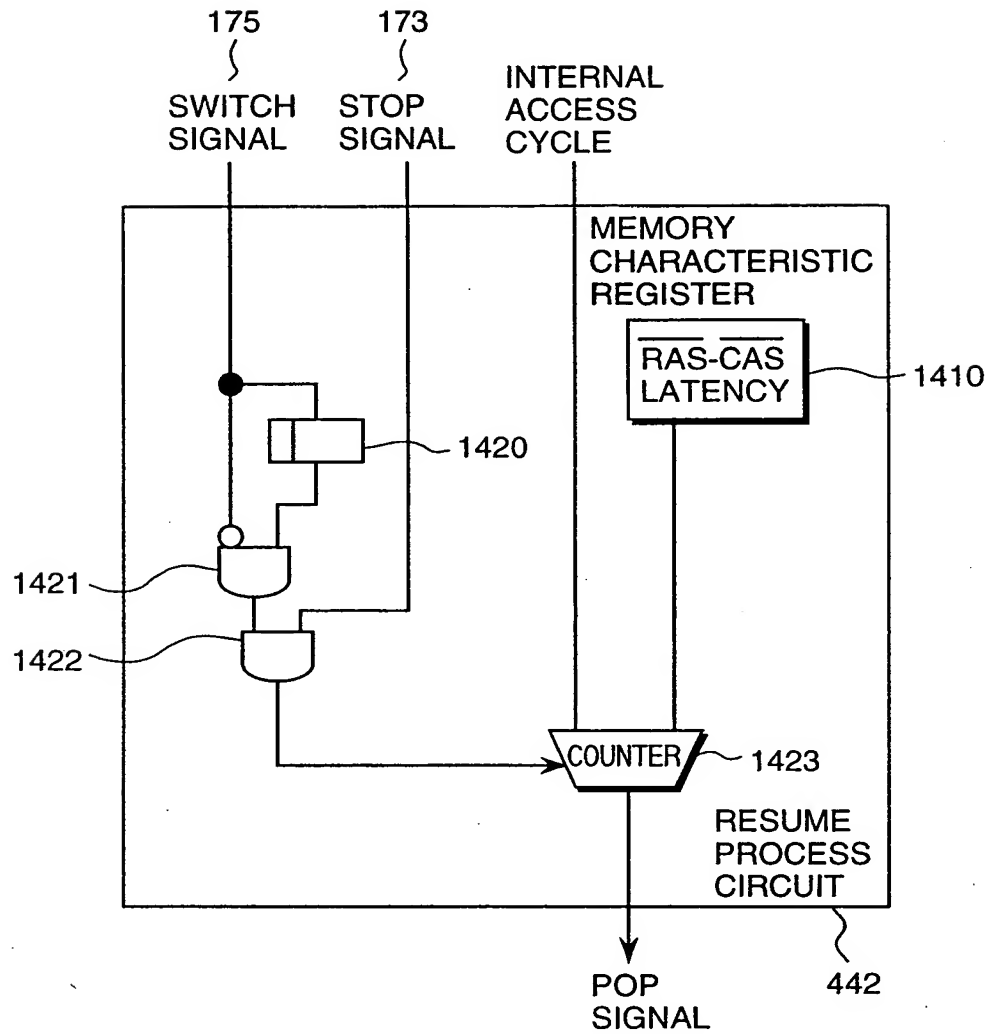


FIG. 15

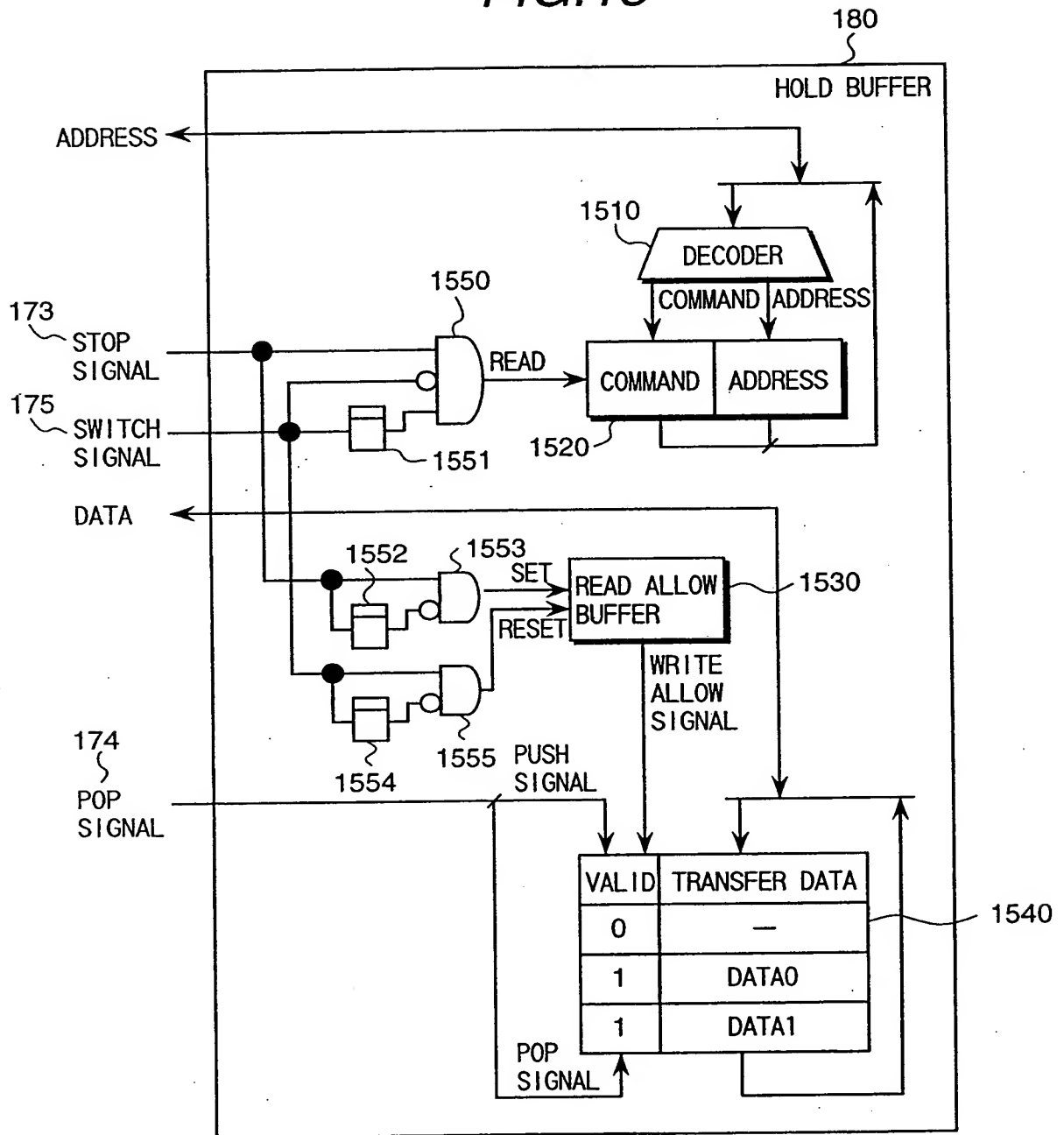
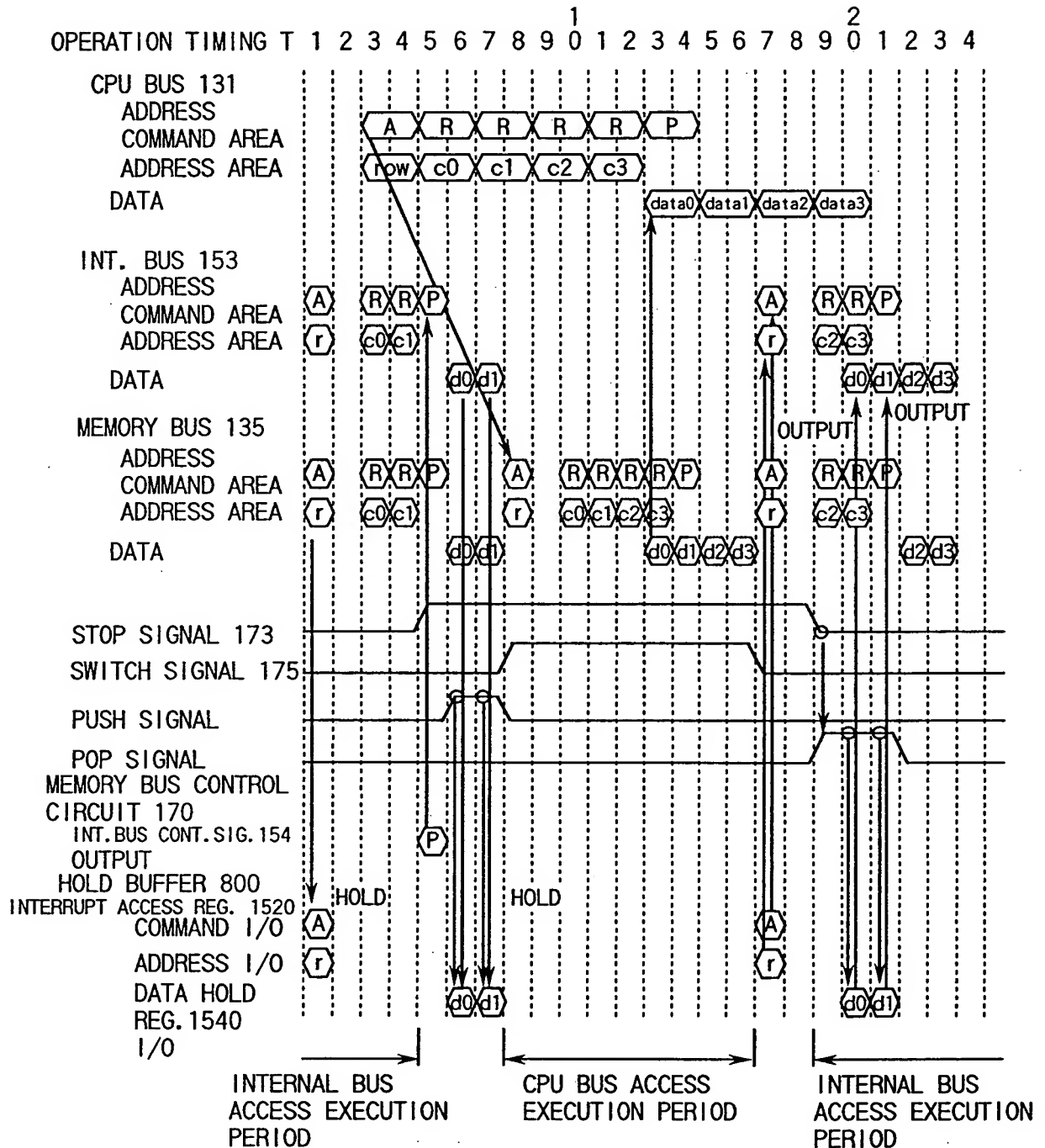


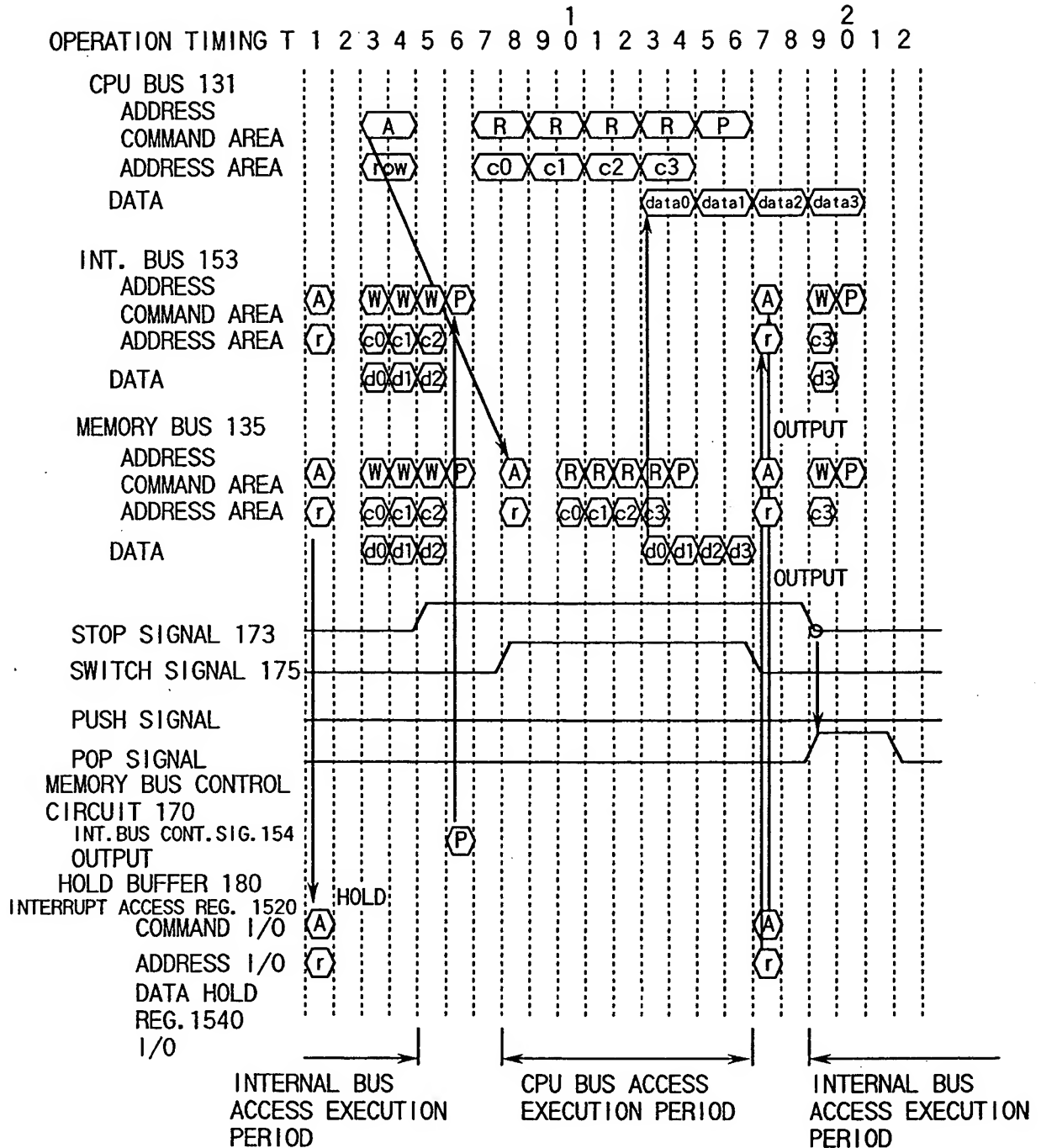
FIG. 16



TRANSFER TIME; 5CYCLES, $\overline{\text{RAS}}$ / $\overline{\text{CAS}}$ LATENCY; 2CYCLES, $\overline{\text{CAS}}$ LATENCY; 3CYCLES, READ PRECHARGE LATENCY; 3CYCLES

A:ACTIVE COMM., R:READ COMM., P: PRECHARGE COMM.
ROW, r:ROW ADDRESS, c:COLUMN ADDRESS, D:DATA

FIG. 17



TRANSFER TIME; 5CYCLES, $\overline{\text{RAS}}/\overline{\text{CAS}}$ LATENCY; 2CYCLES, $\overline{\text{CAS}}$ LATENCY; 3CYCLES, READ PRECHARGE LATENCY; 3CYCLES

A:ACTIVE COMM., R:READ COMM., W:WRITE COMM.,
P:PRECHARGE COMM.

ROW, r:ROW ADDRESS, c:COLUMN ADDRESS, D:DATA

FIG. 18

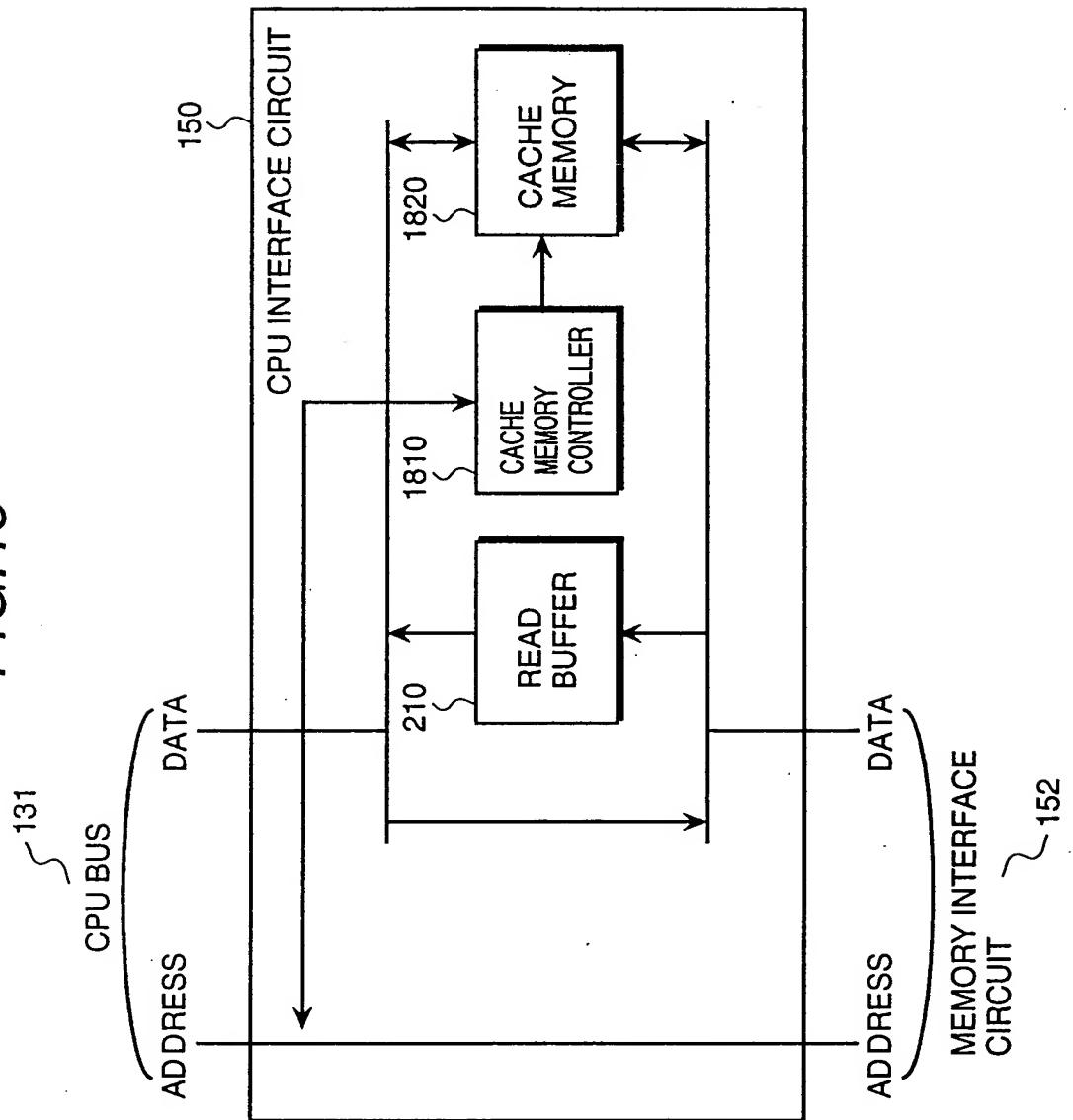


FIG. 19

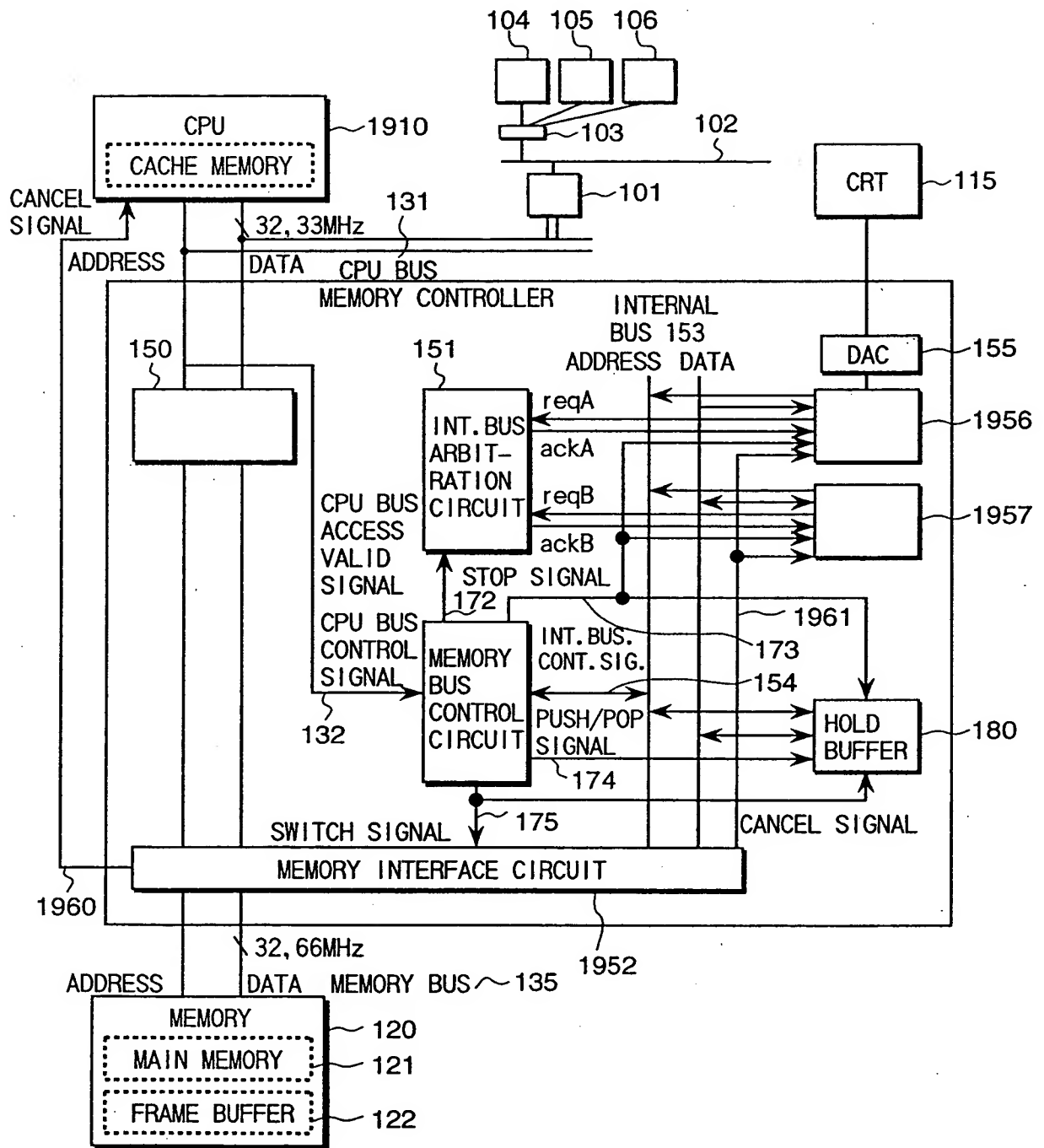


FIG. 20

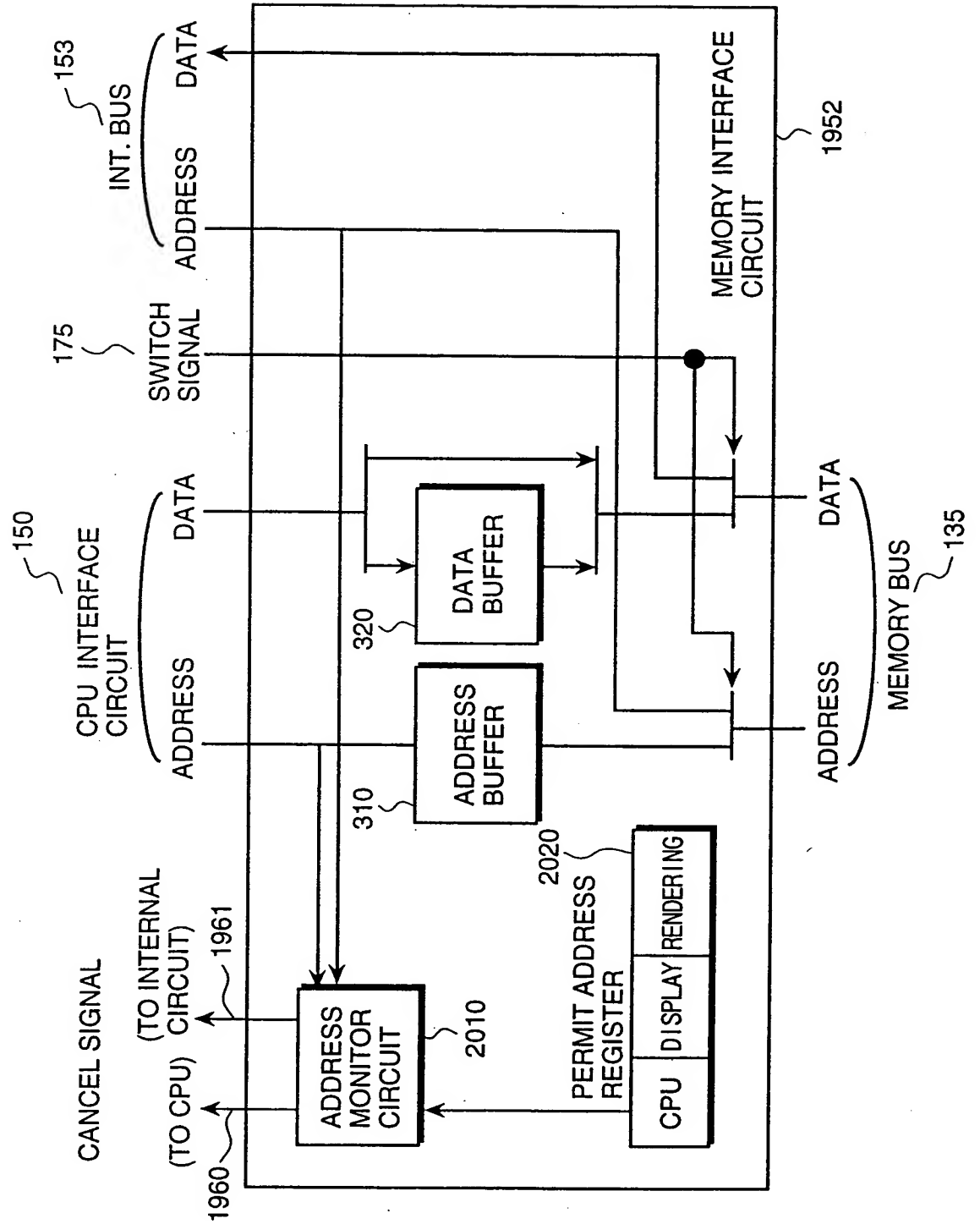


FIG.21

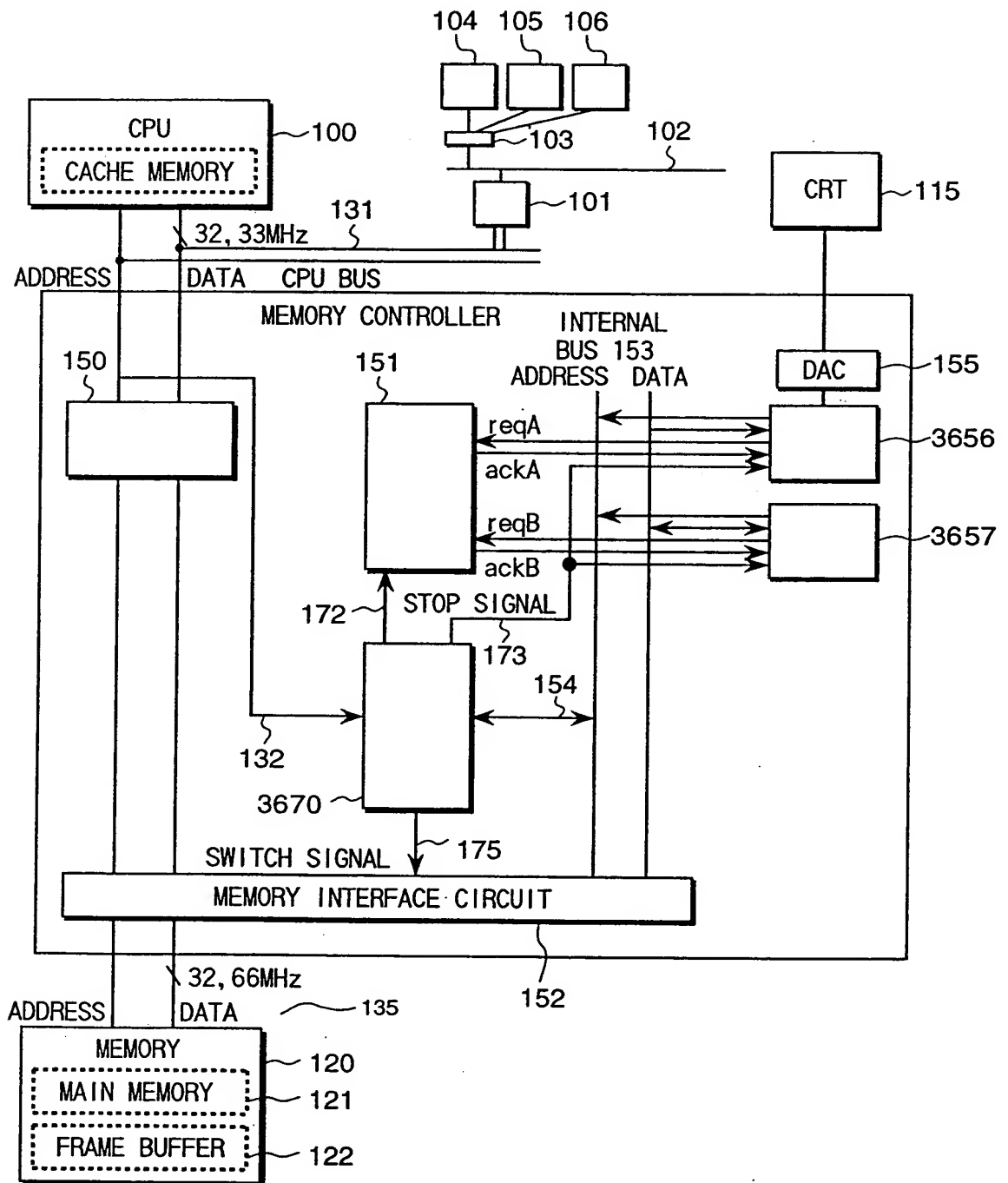


FIG.22

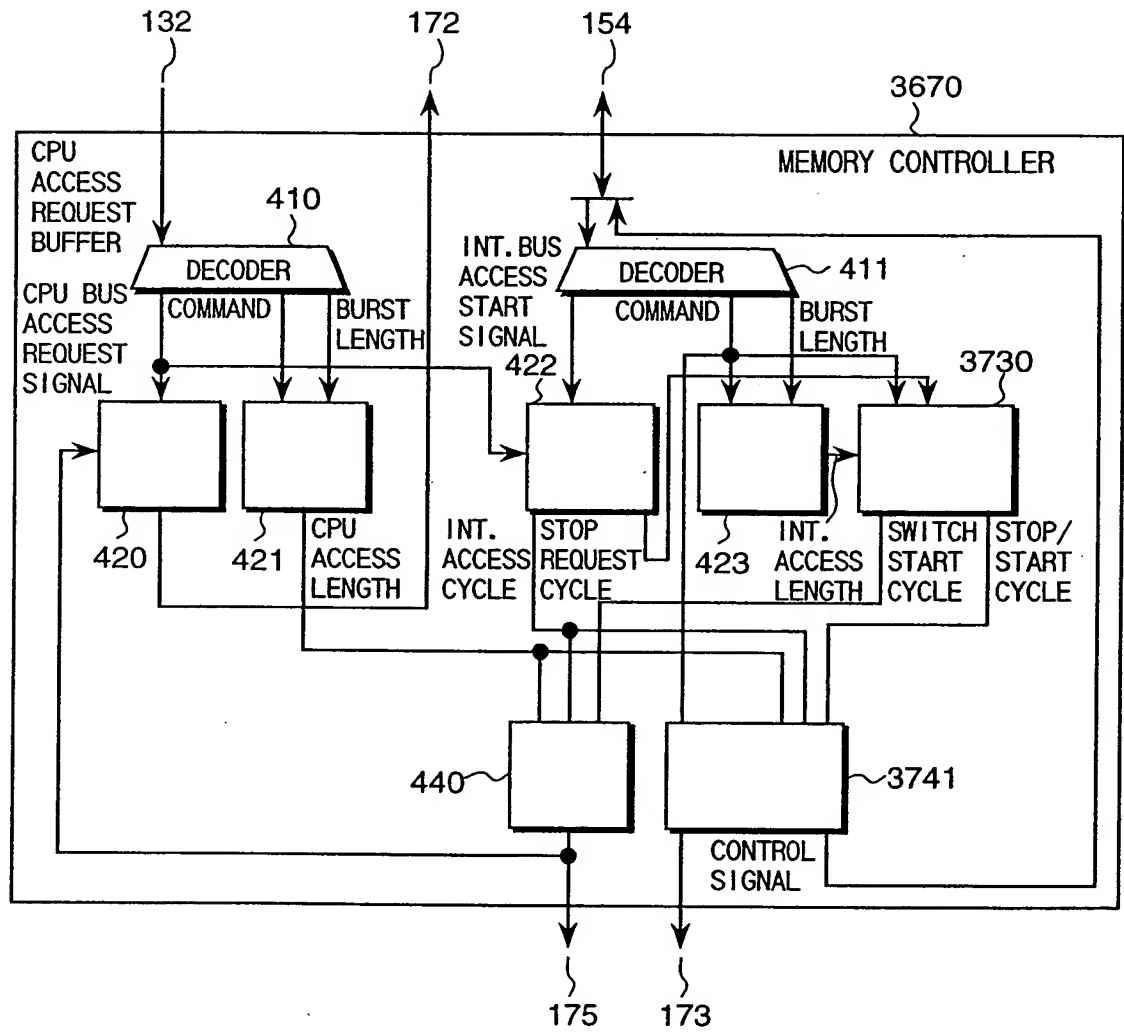


FIG. 23

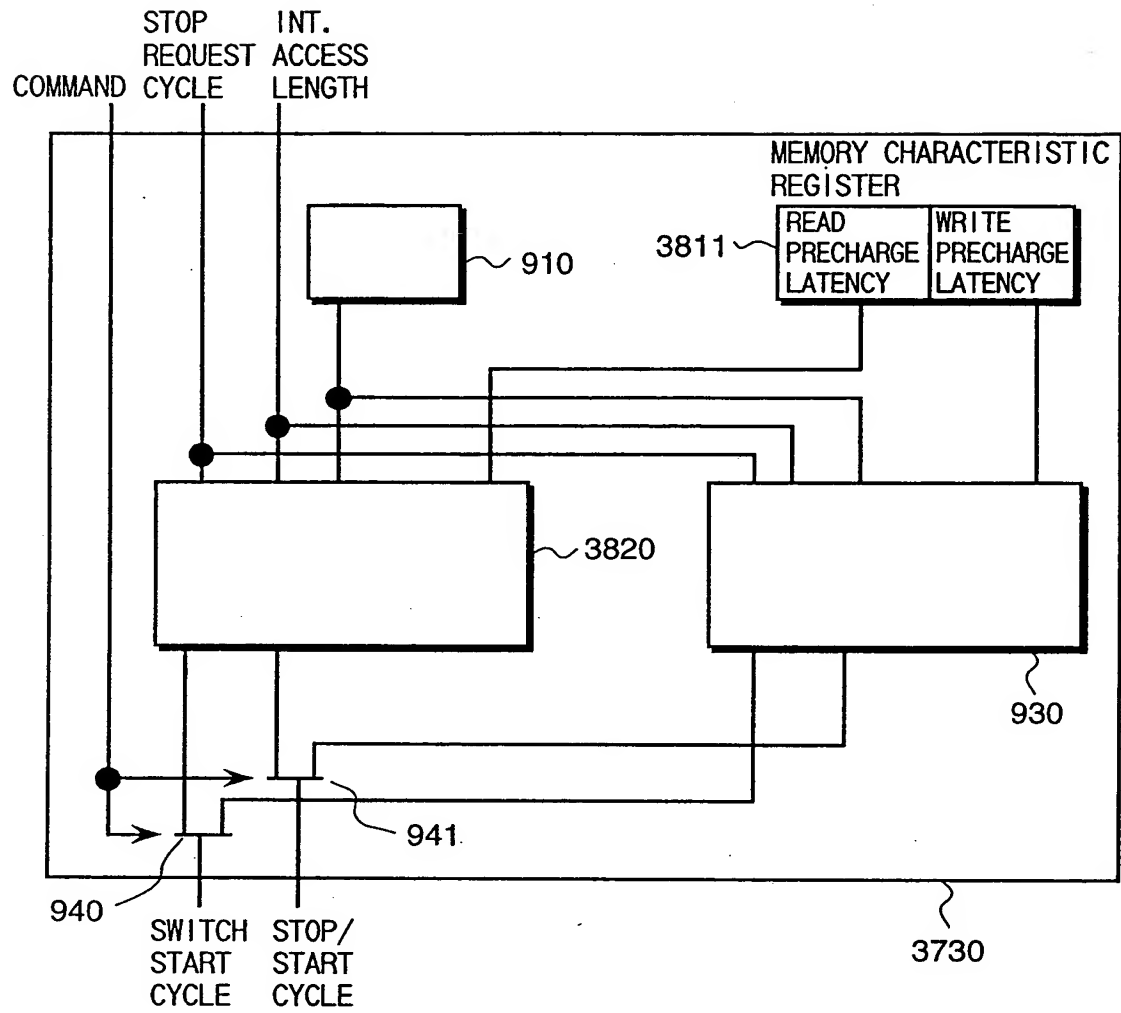


FIG.24

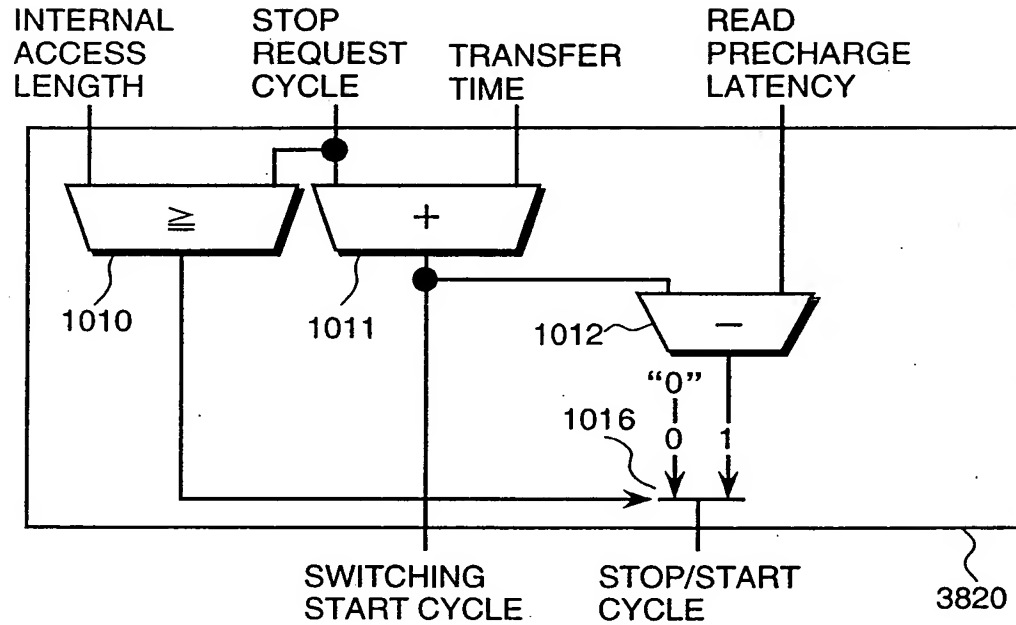


FIG.25

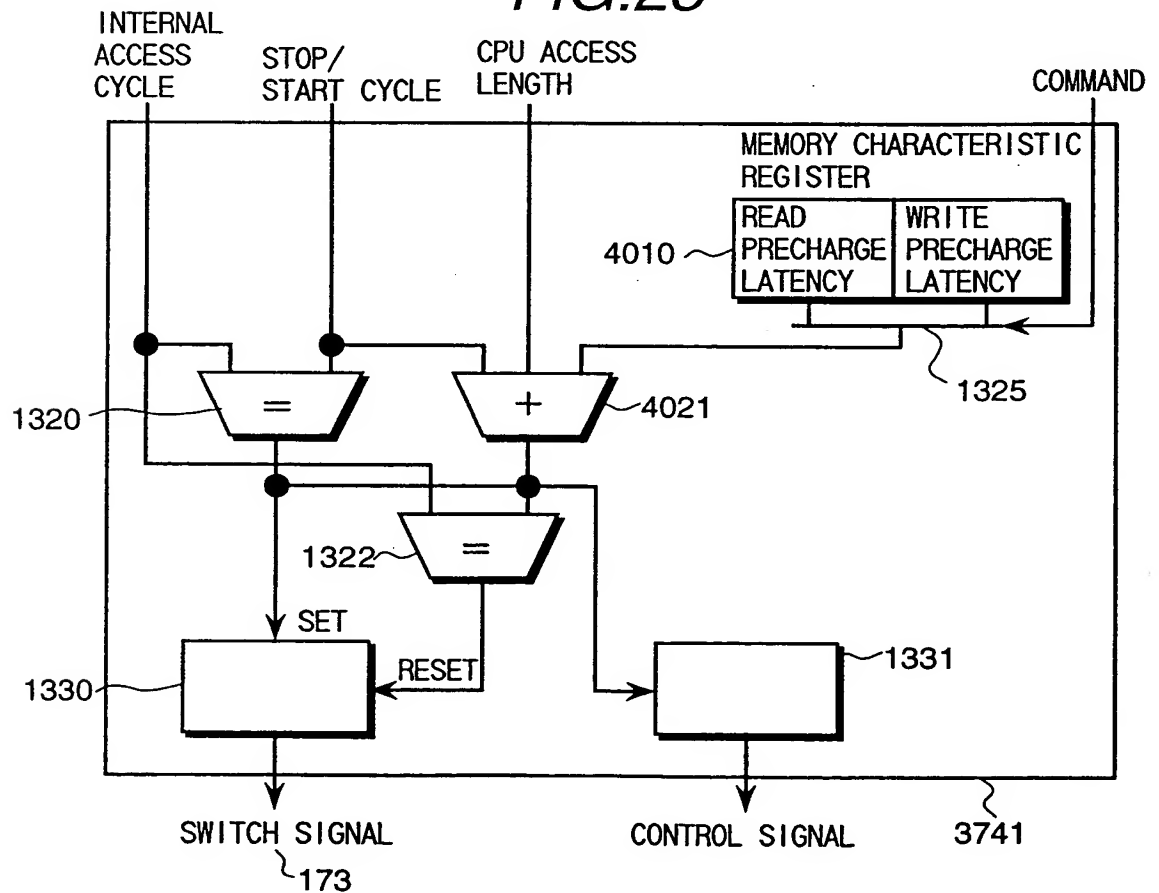
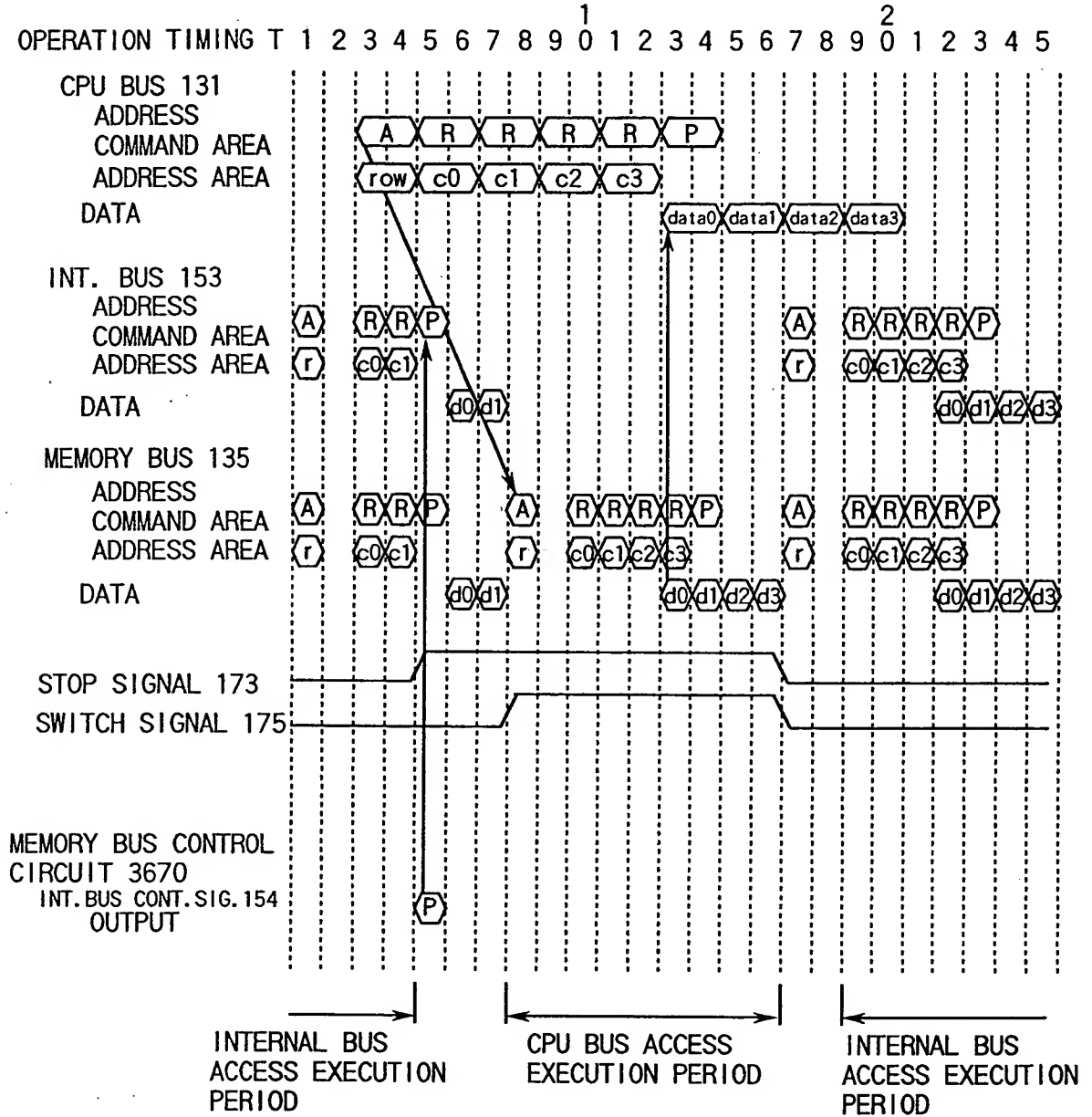


FIG.26



TRANSFER TIME; 5CYCLES, $\overline{\text{RAS}}$ / $\overline{\text{CAS}}$ LATENCY; 2CYCLES, $\overline{\text{CAS}}$ LATENCY; 3CYCLES, READ PRECHARGE LATENCY; 3CYCLES

A:ACTIVE COMM., R:READ COMM., P: PRECHARGE COMM.
 ROW, r:ROW ADDRESS, c:COLUMN ADDRESS, D:DATA

FIG.27

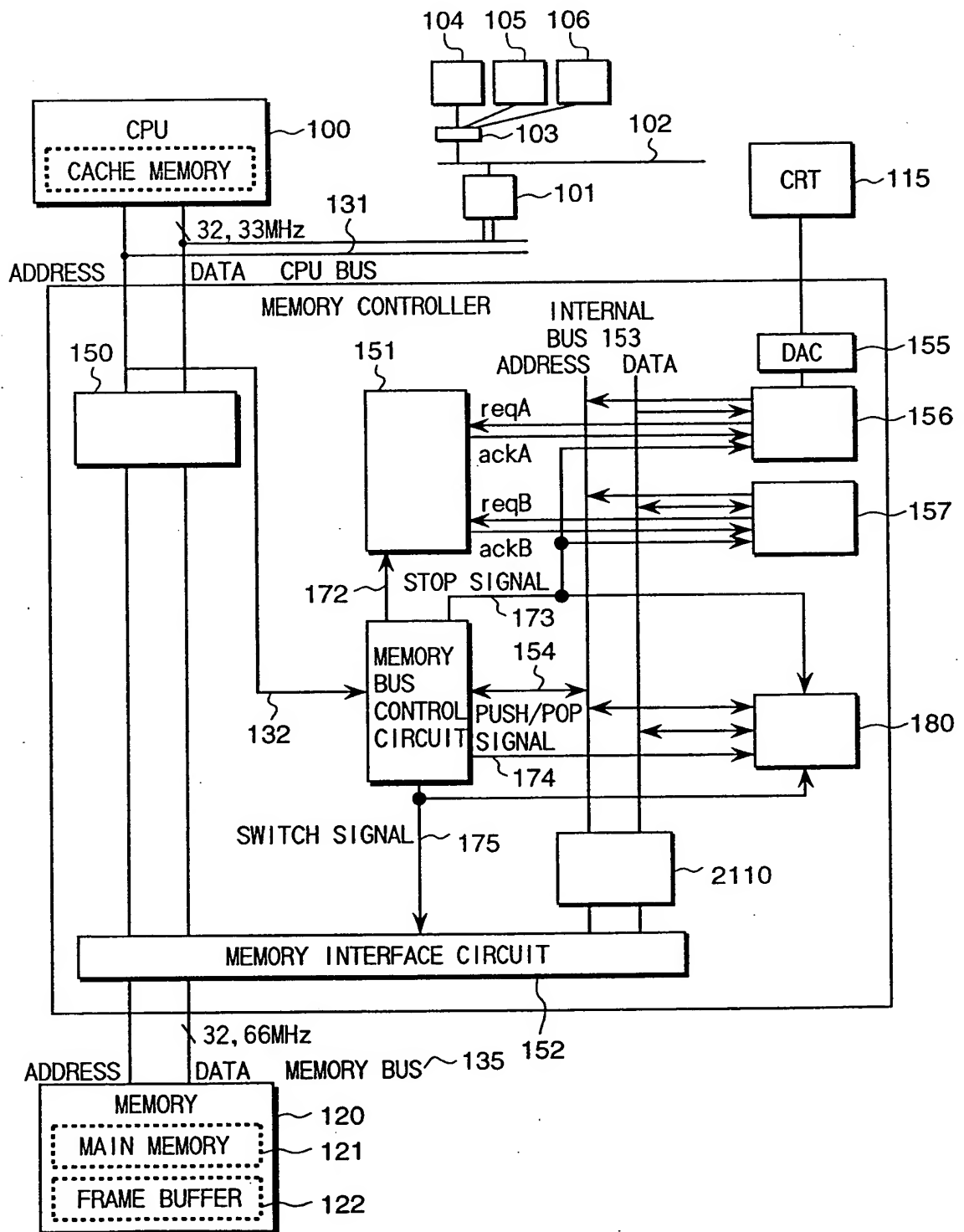


FIG. 28

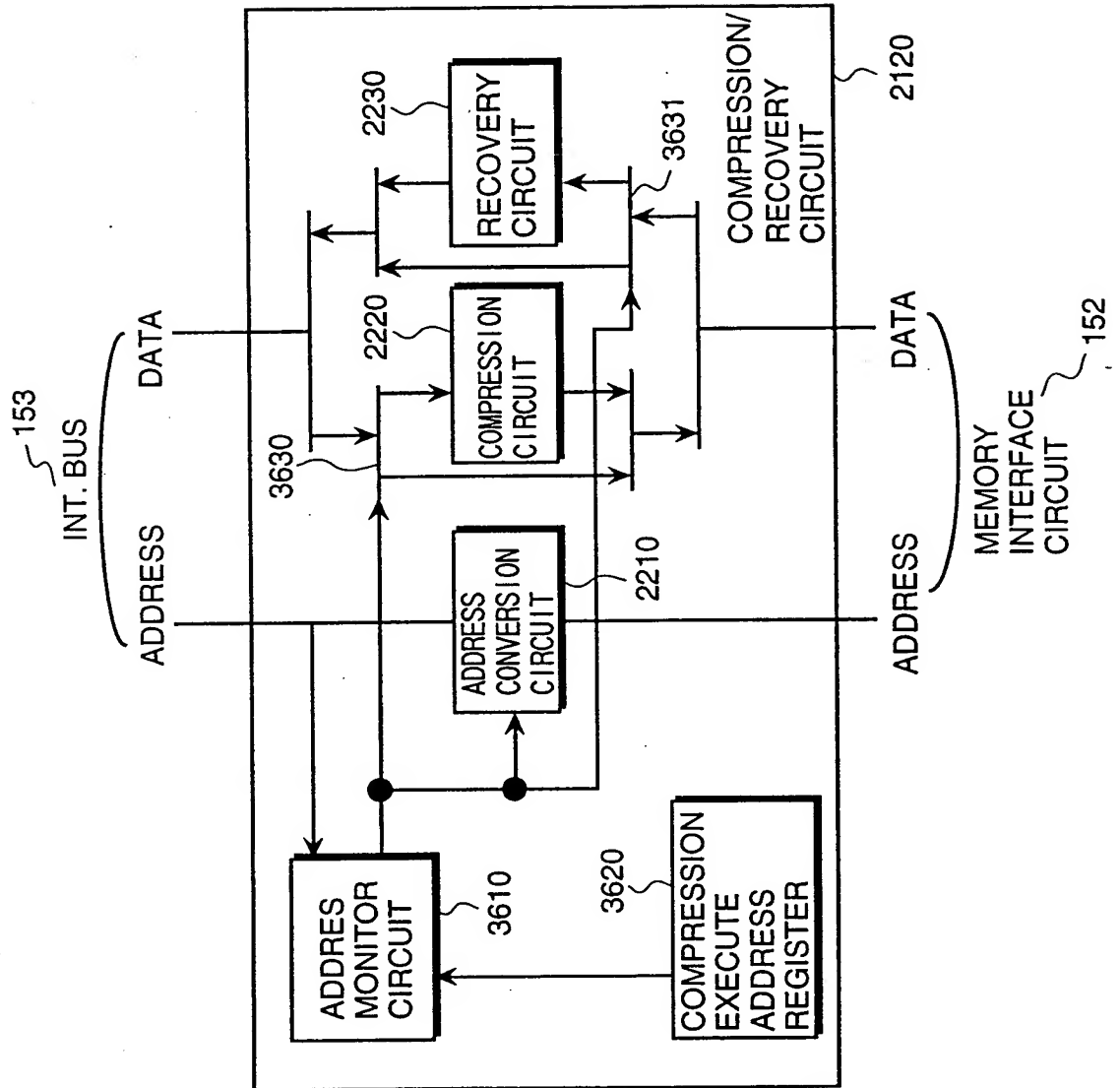


FIG.29

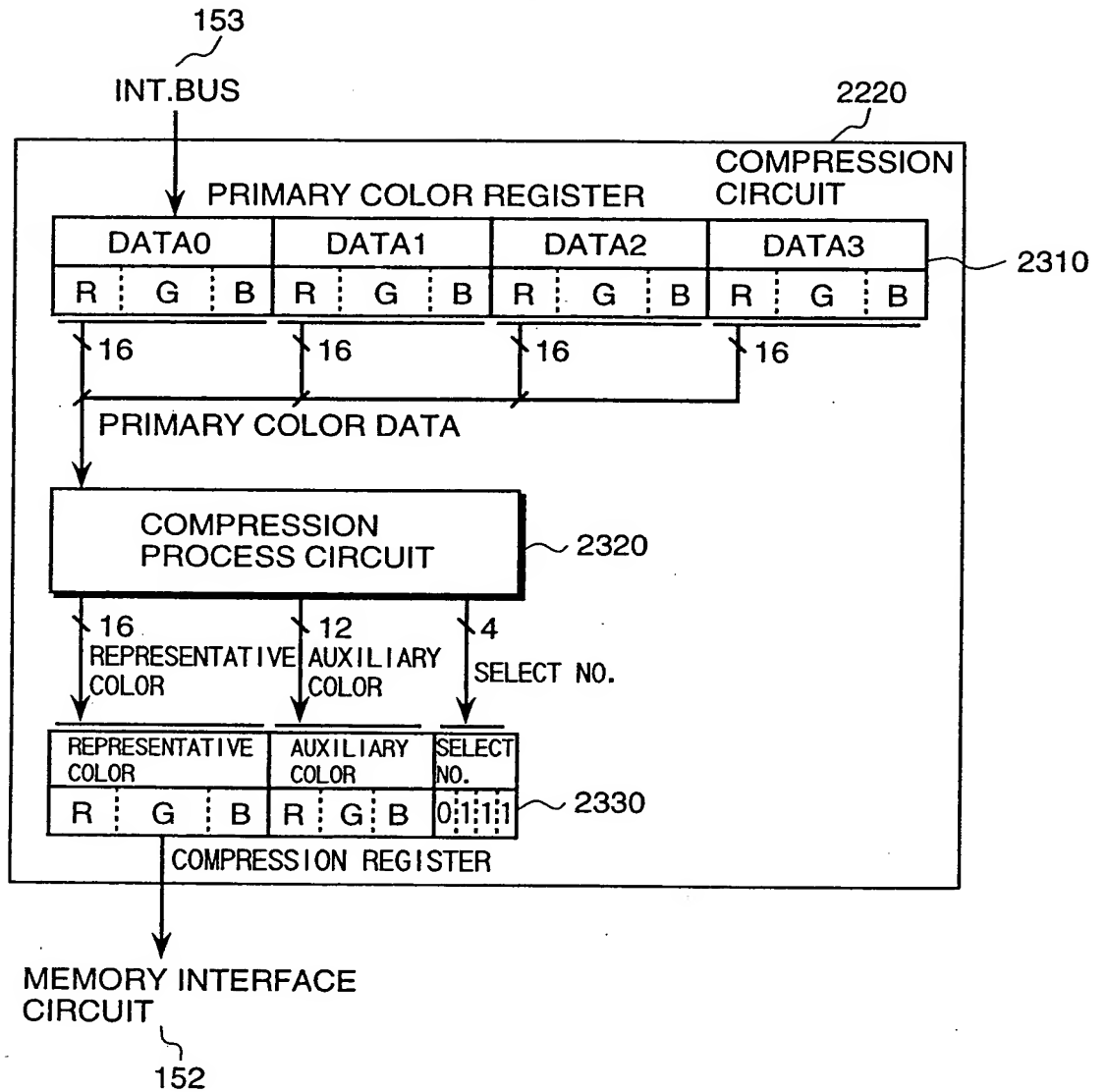


FIG.30

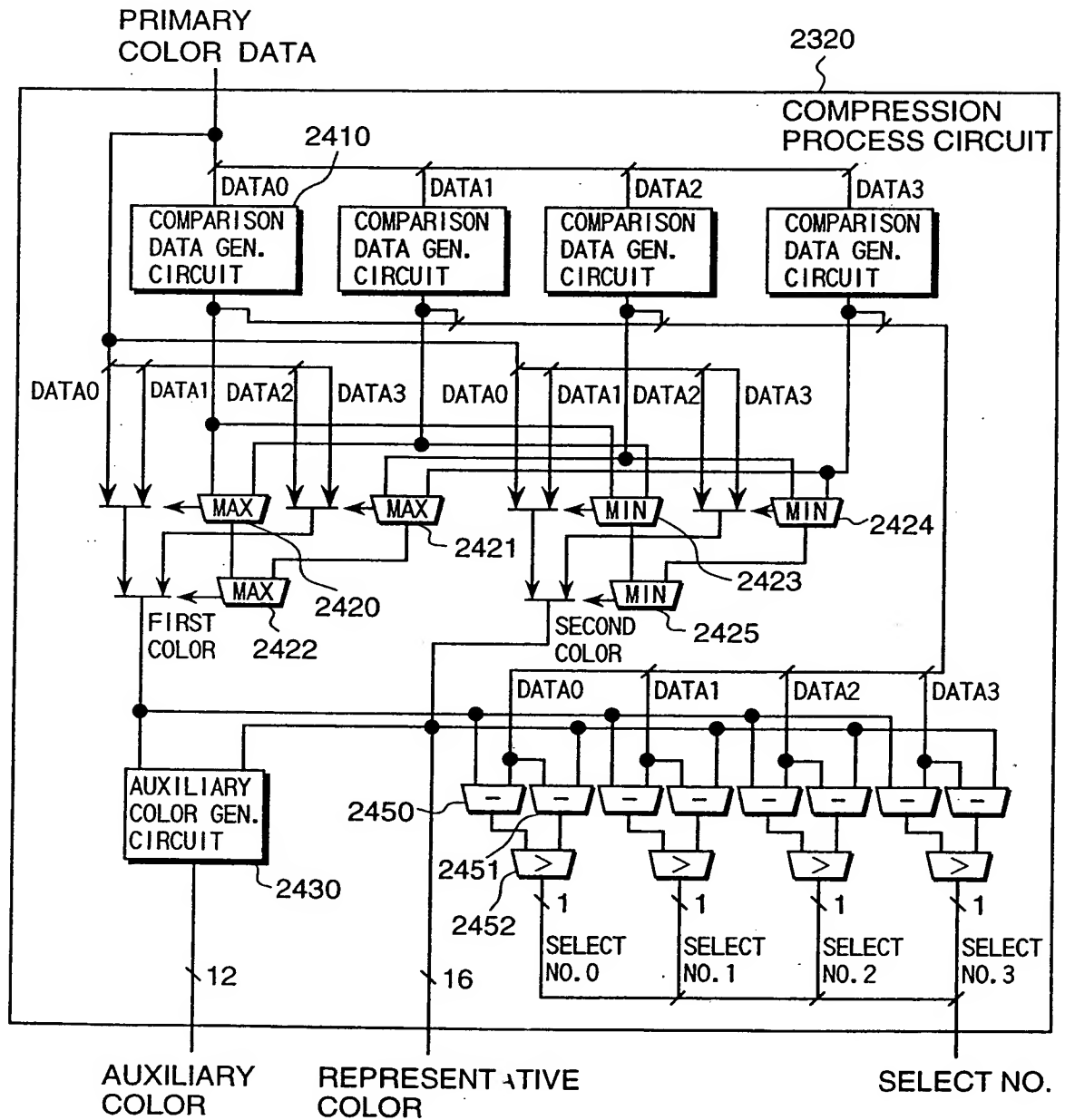


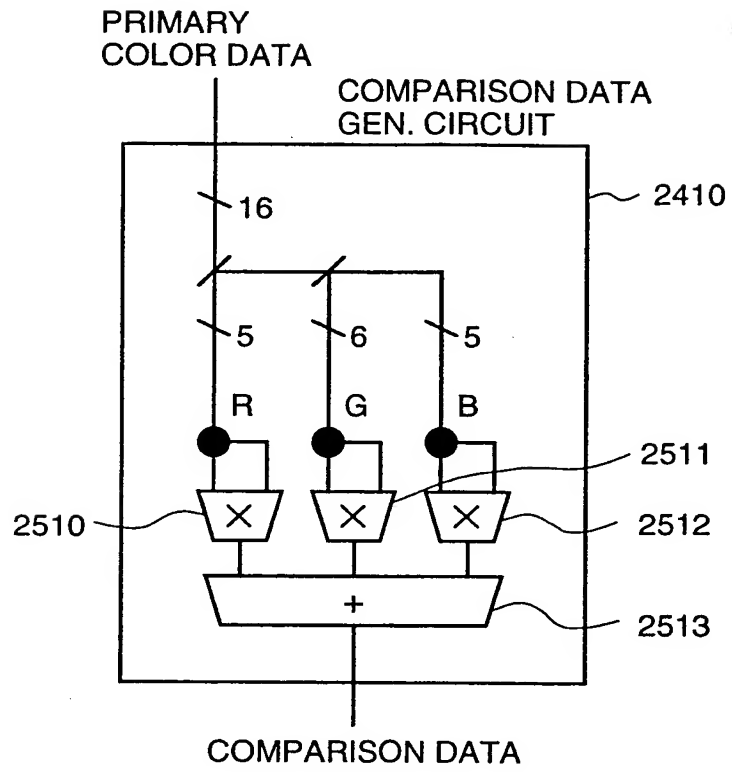
FIG.31

FIG.32

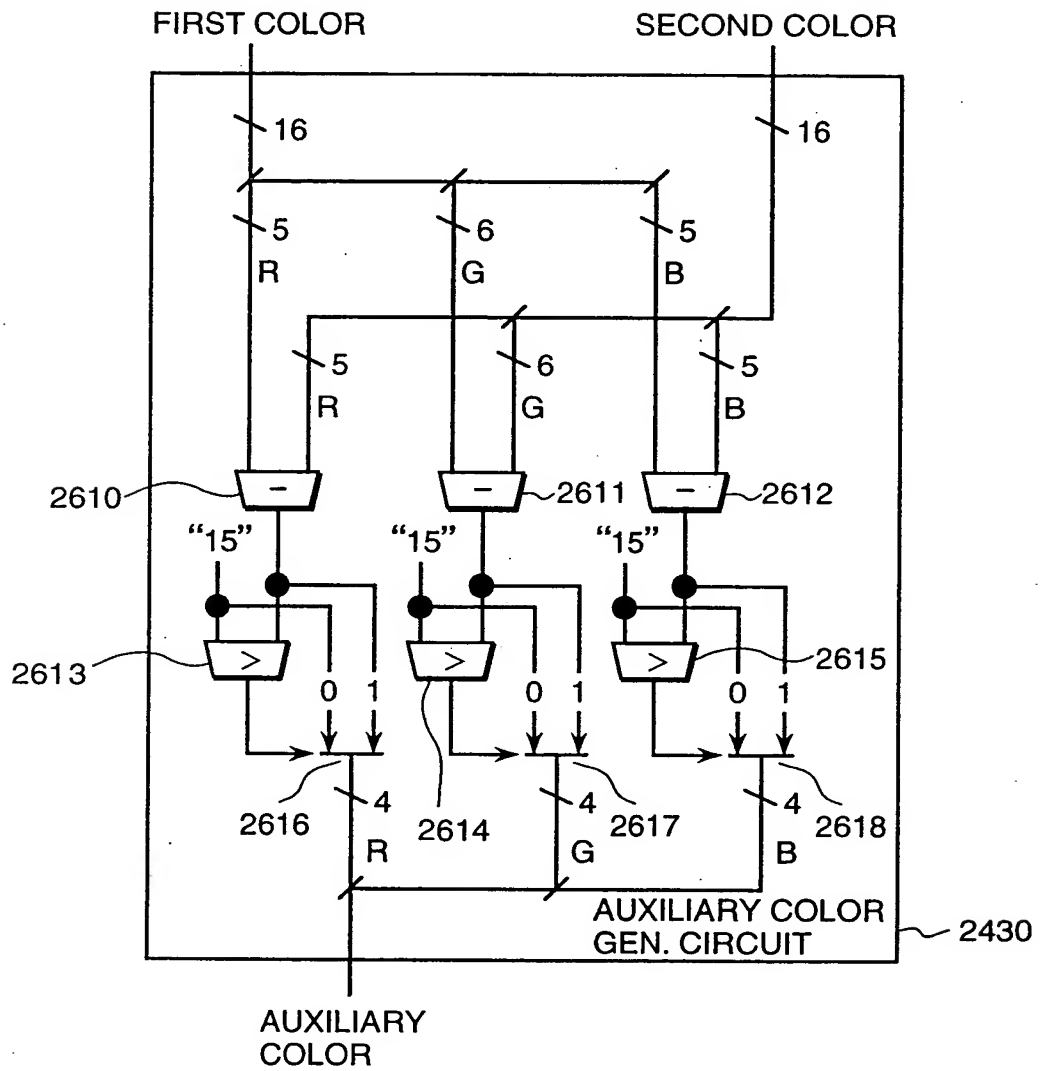


FIG.33

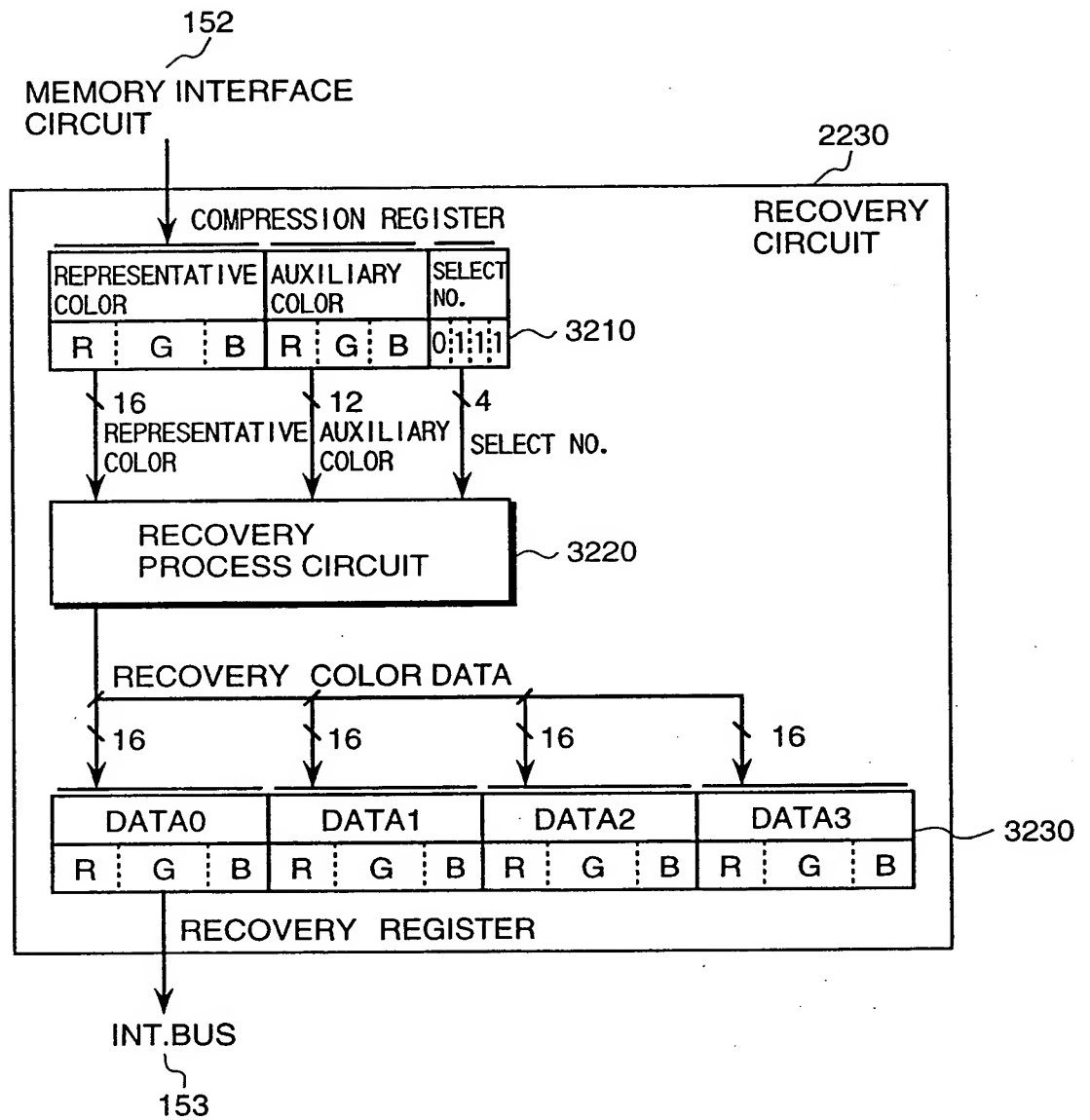


FIG. 34

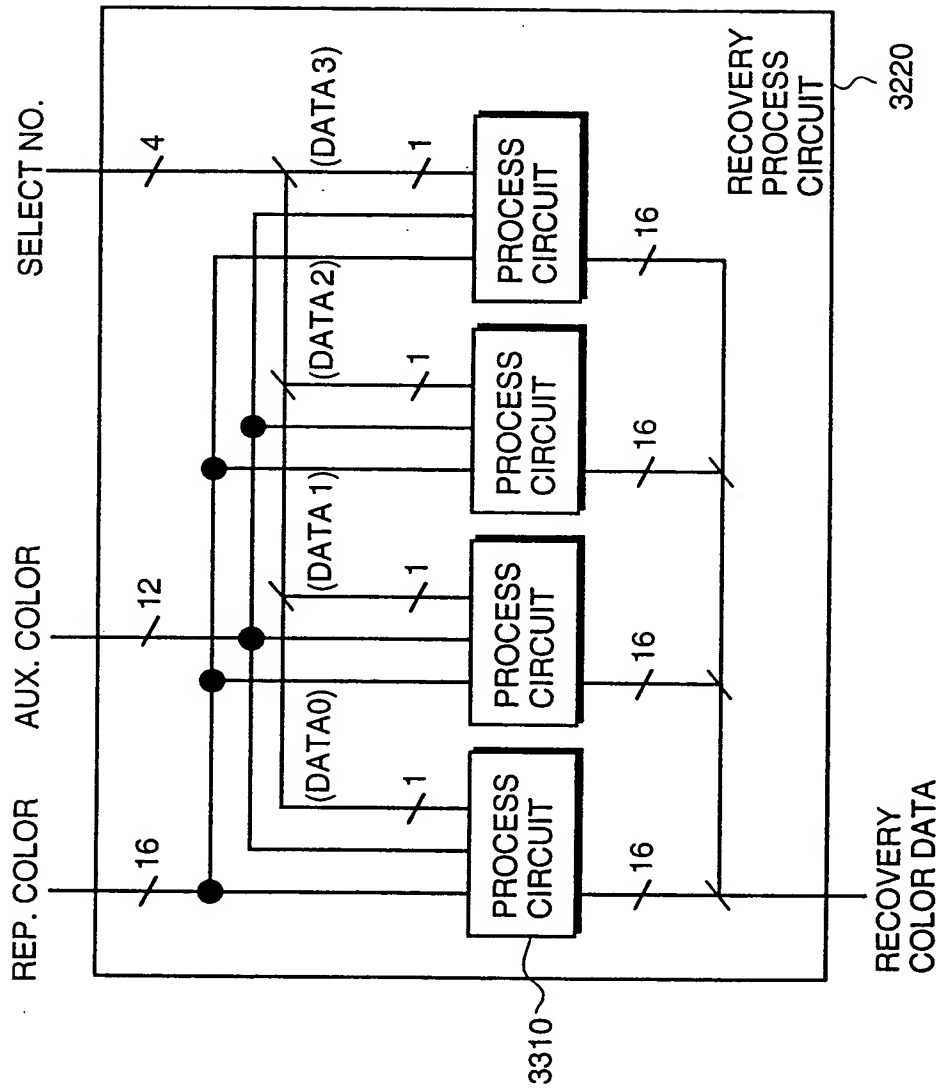


FIG. 35

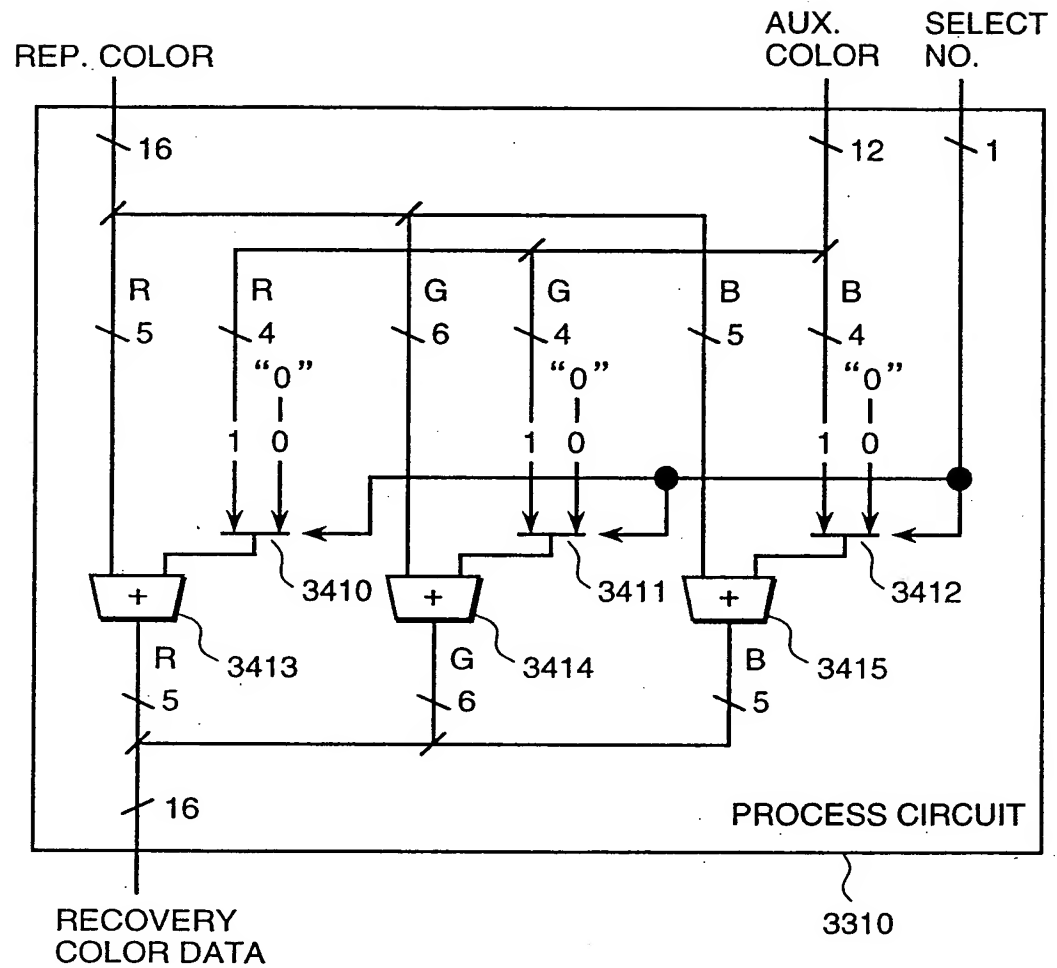


FIG.36

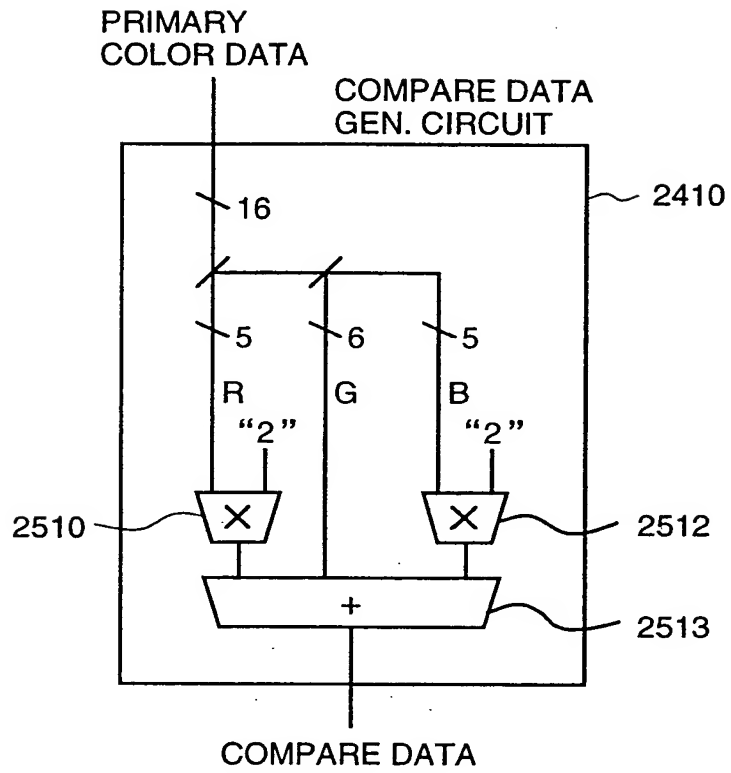


FIG.37

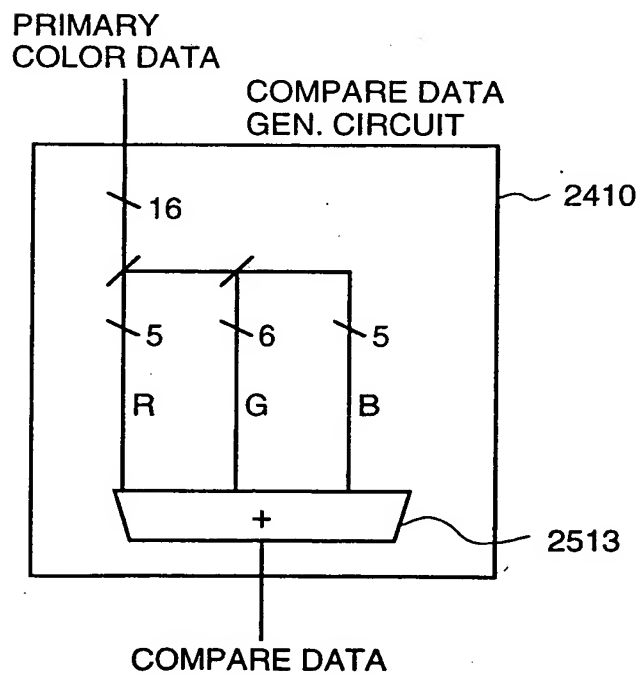


FIG.38

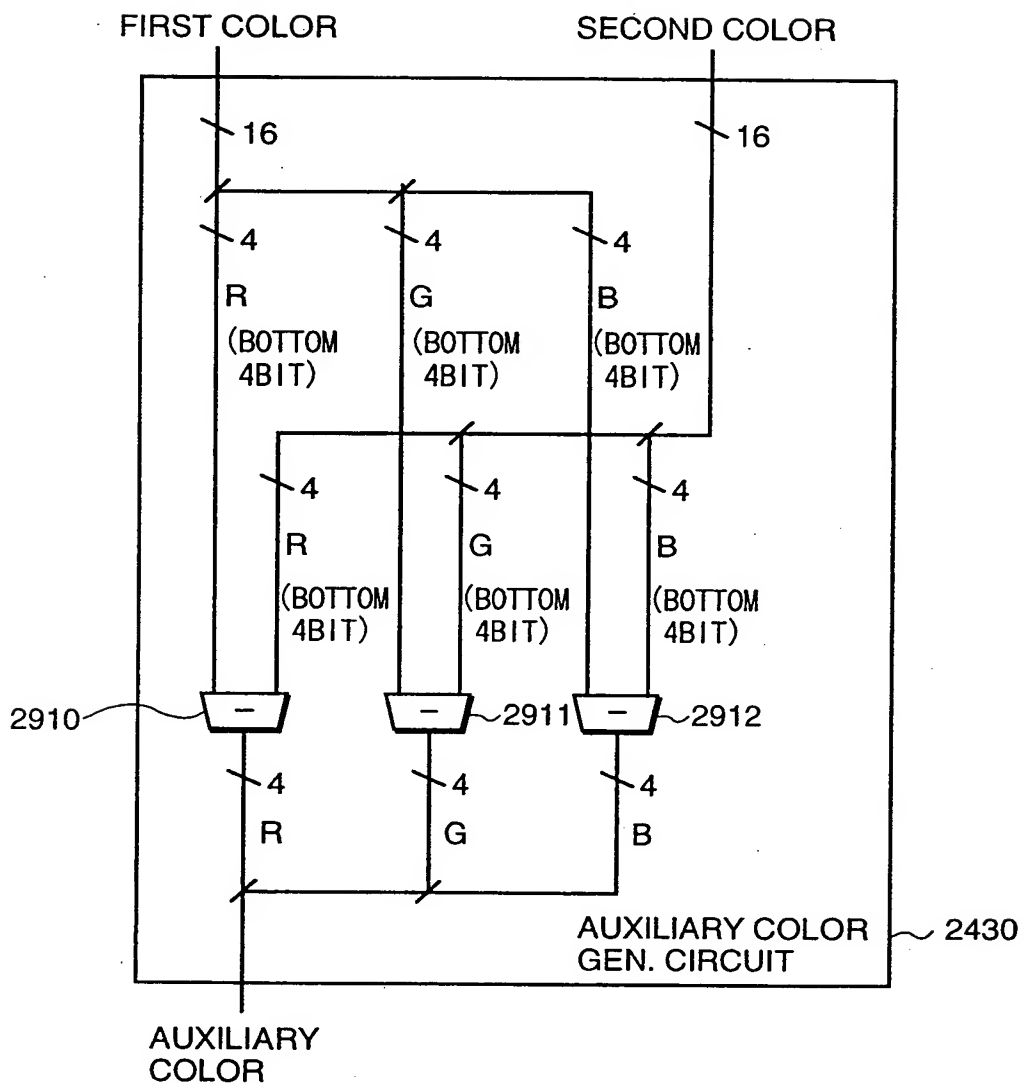


FIG. 39

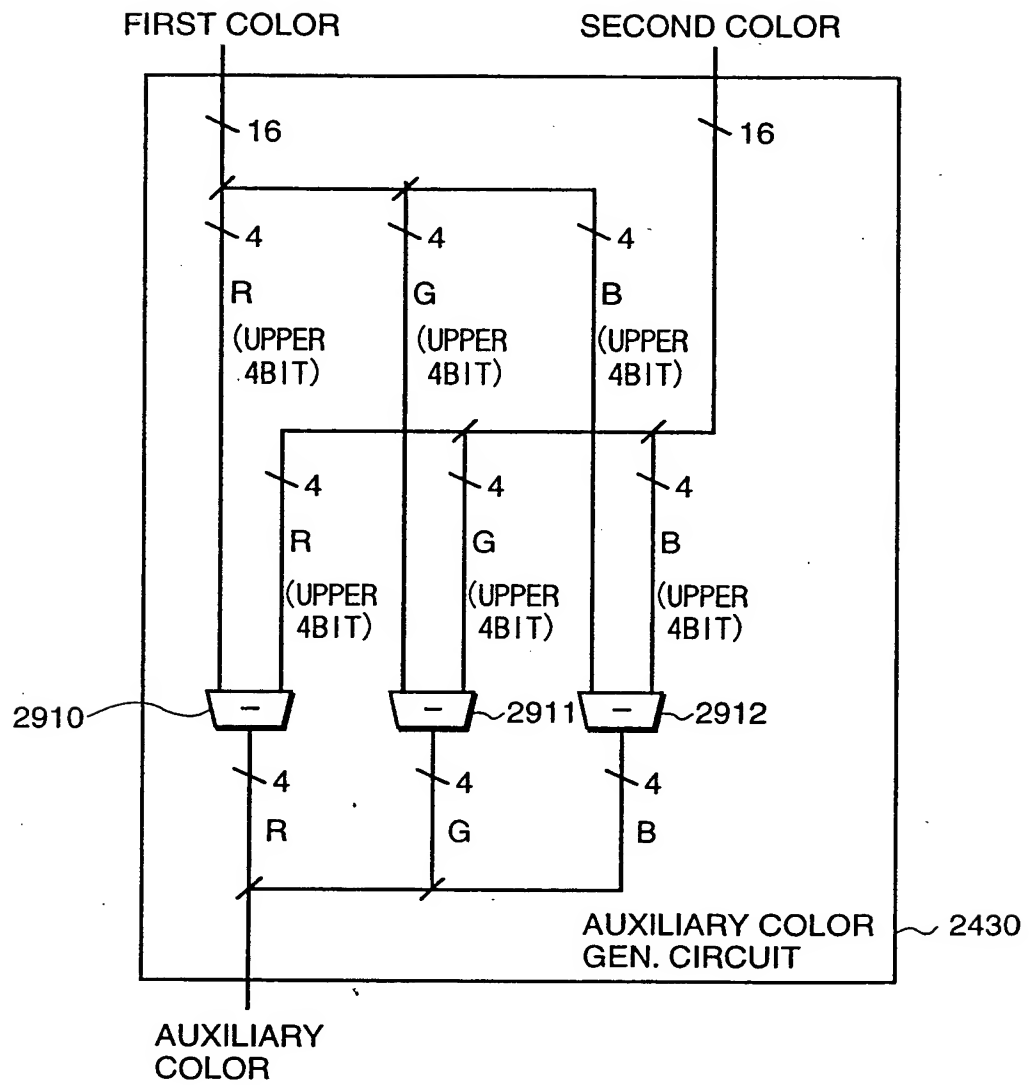


FIG.40

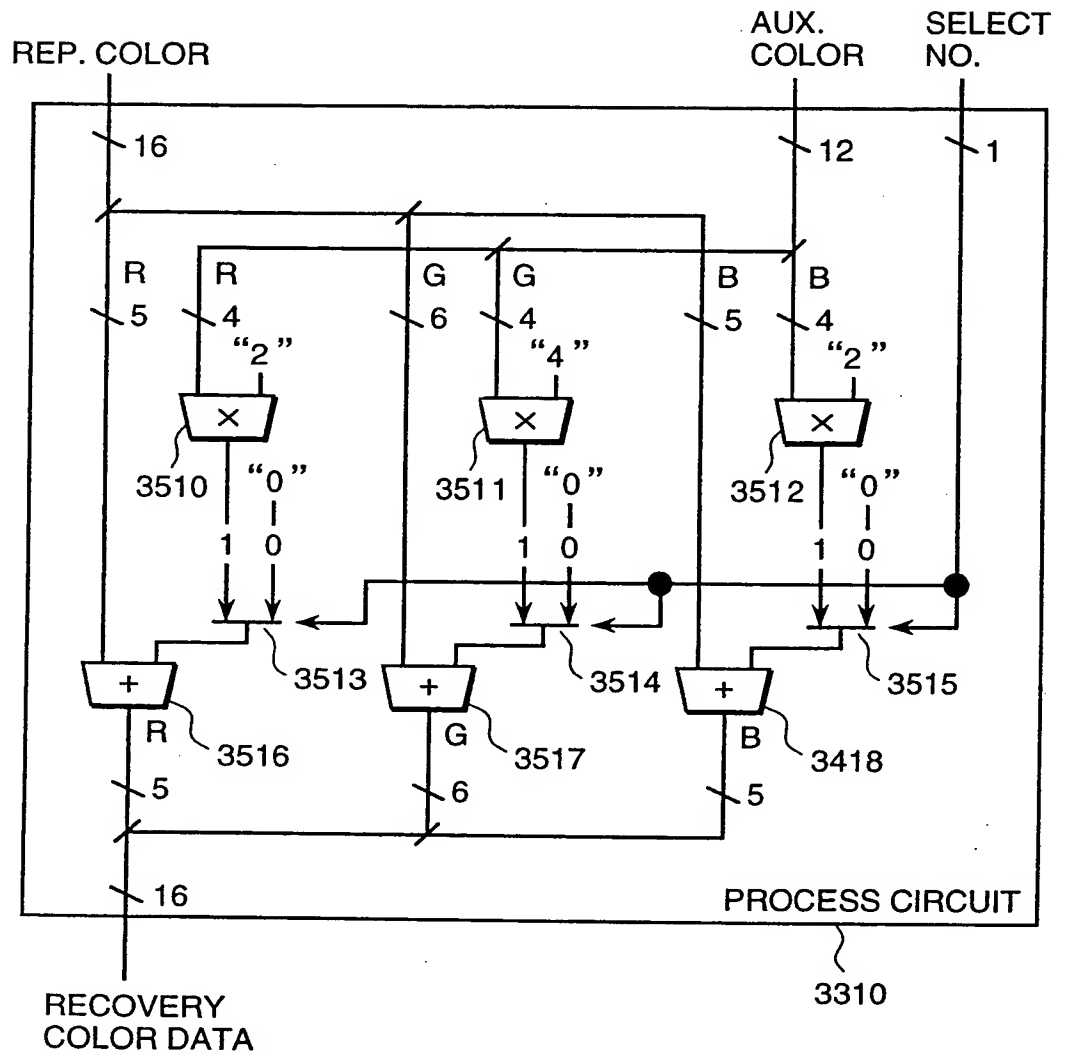


FIG. 41

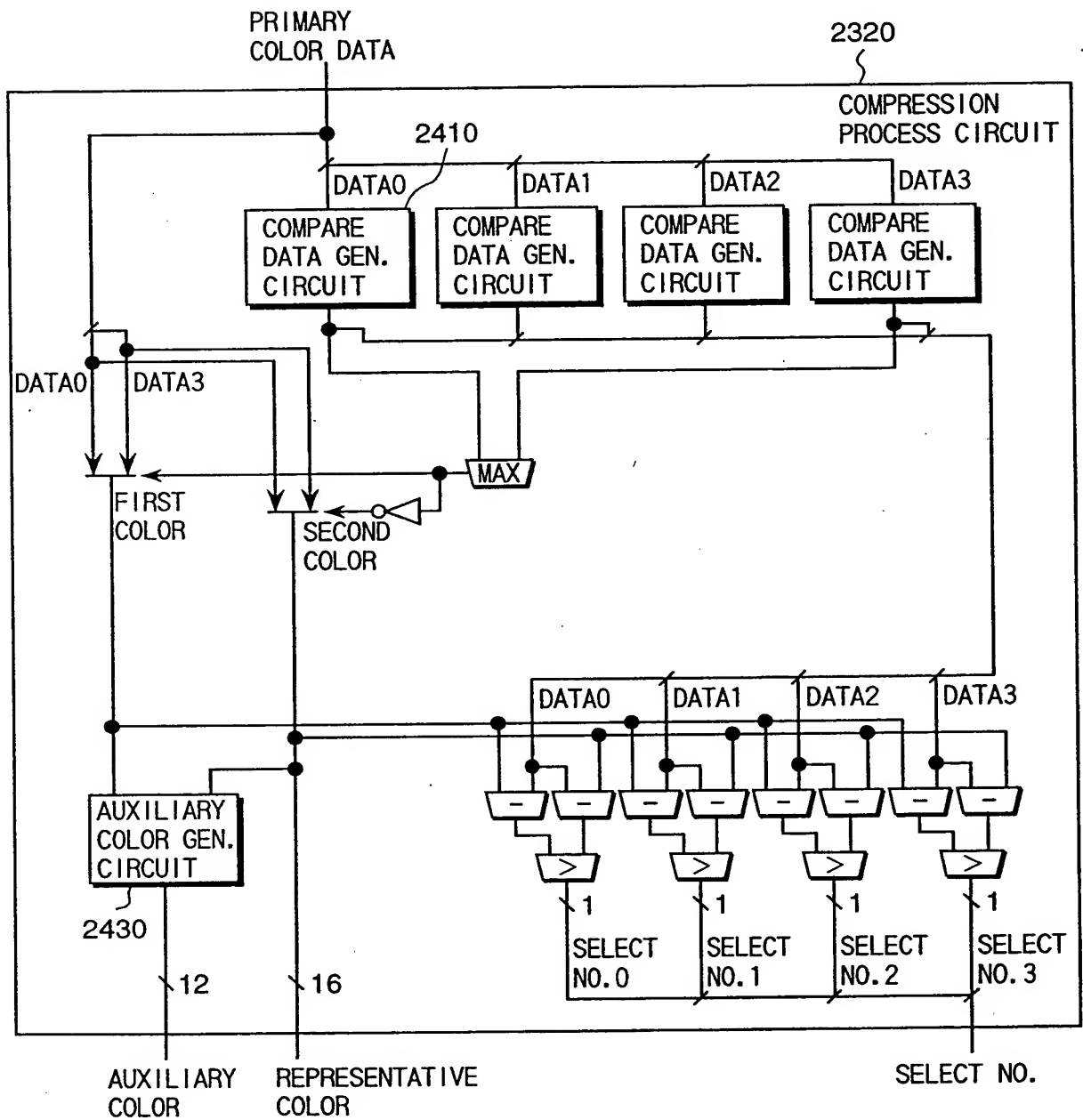


FIG. 42

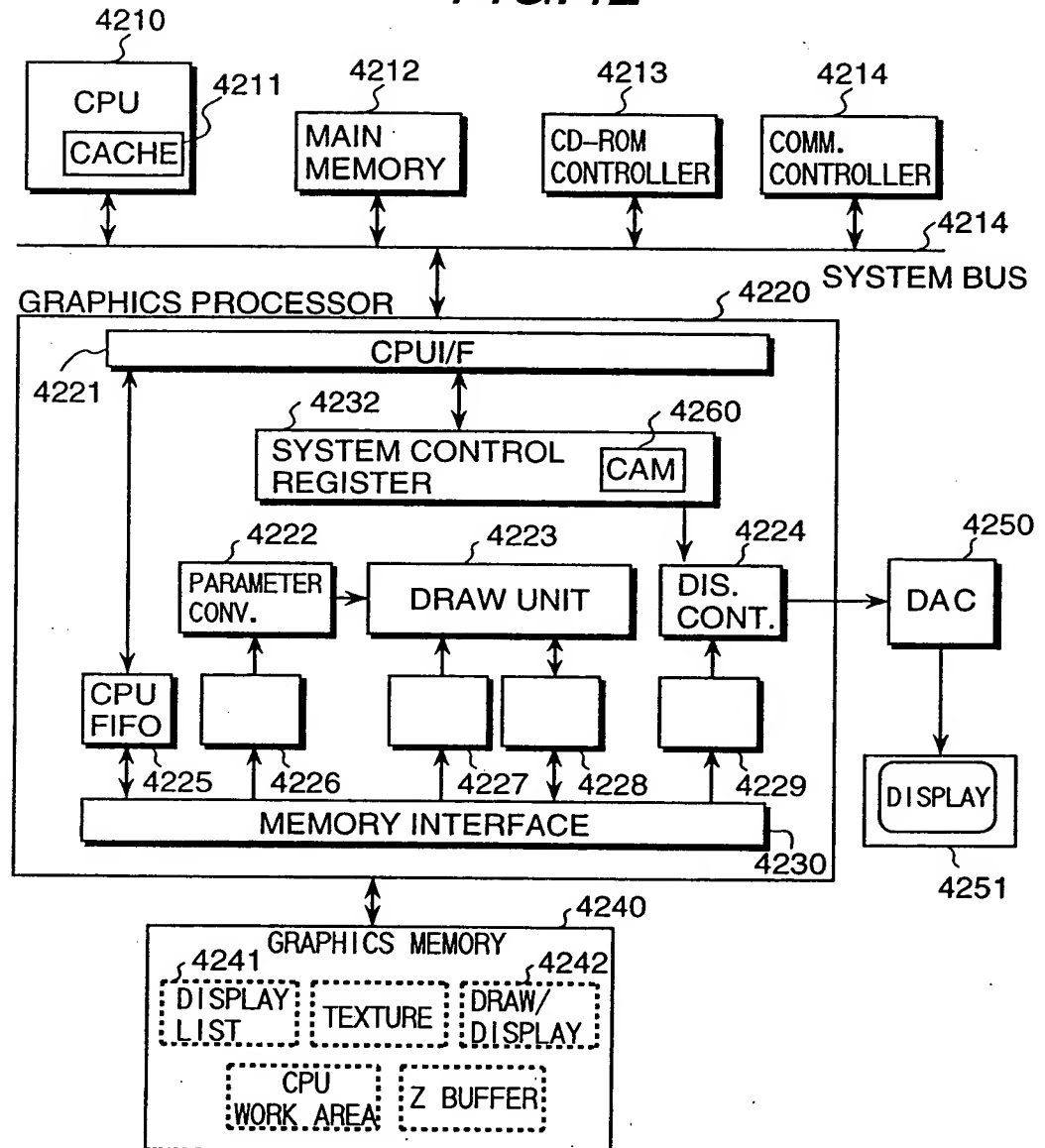


FIG.43

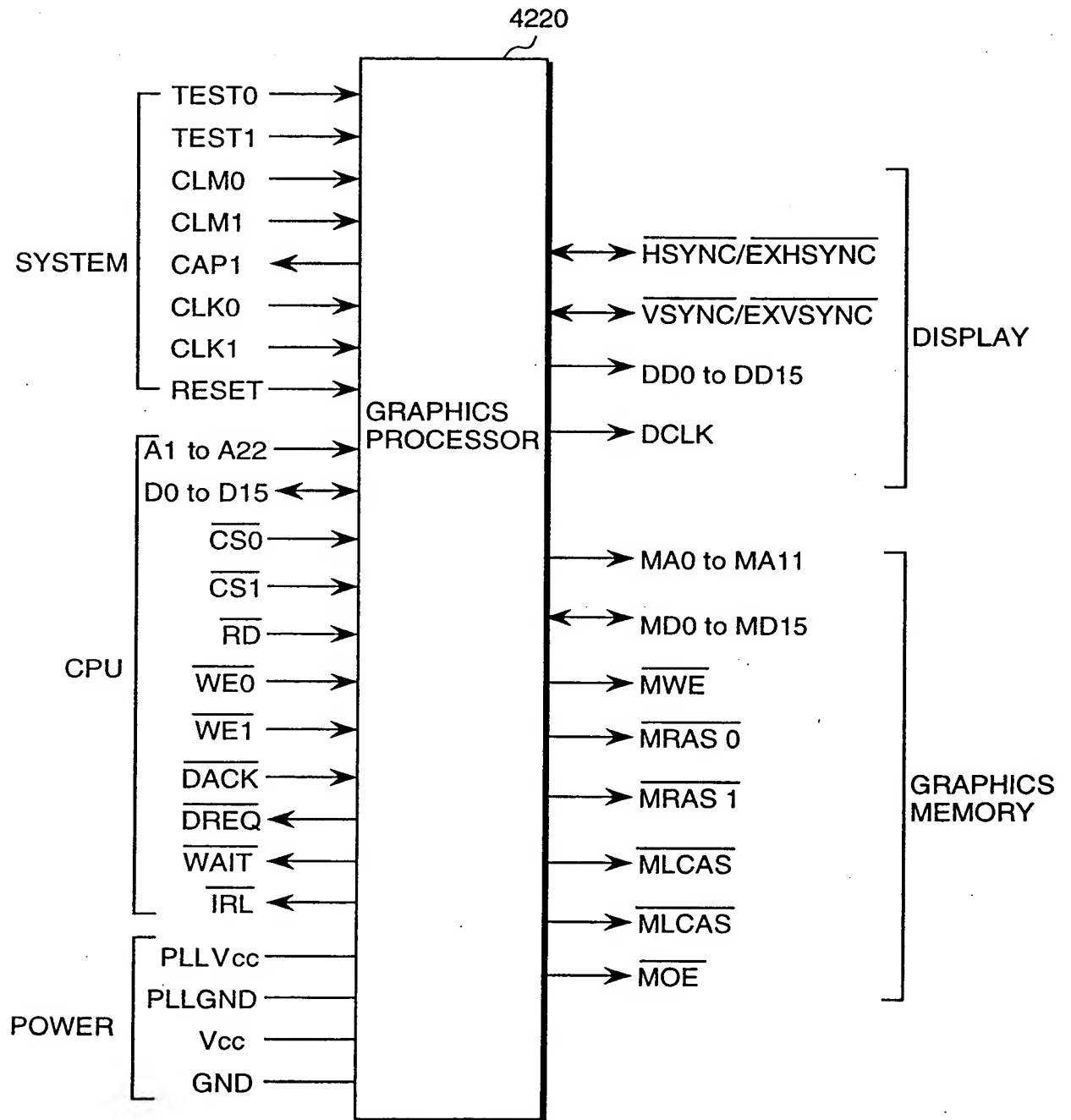


FIG.44

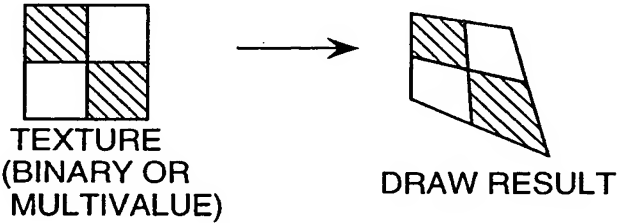

COMMAND	FUNCTION
DRAW SQUARE	DEFORM SQUARE TEXTURE PATTERN  TEXTURE (BINARY OR MULTIVALUE) DRAW RESULT
LINE	DRAW A LINE 
MOVE	MOVE DRAW-START POINT
LOFS	SET LOCAL OFF-SET PARAMETER
AFFIN	SET AFFIN TRANSFORM PARAMETER
JUMP	BRANCH DISPLAY LIST
GOSUB	CALL SUBROUTINE
RET	RETURN FROM SUBROUTINE
TRAP	TERMINATE DISPLAY LIST FETCH
FLASH	FLASH SOURCE CACHE

FIG. 45

[illegible]

FIG. 46

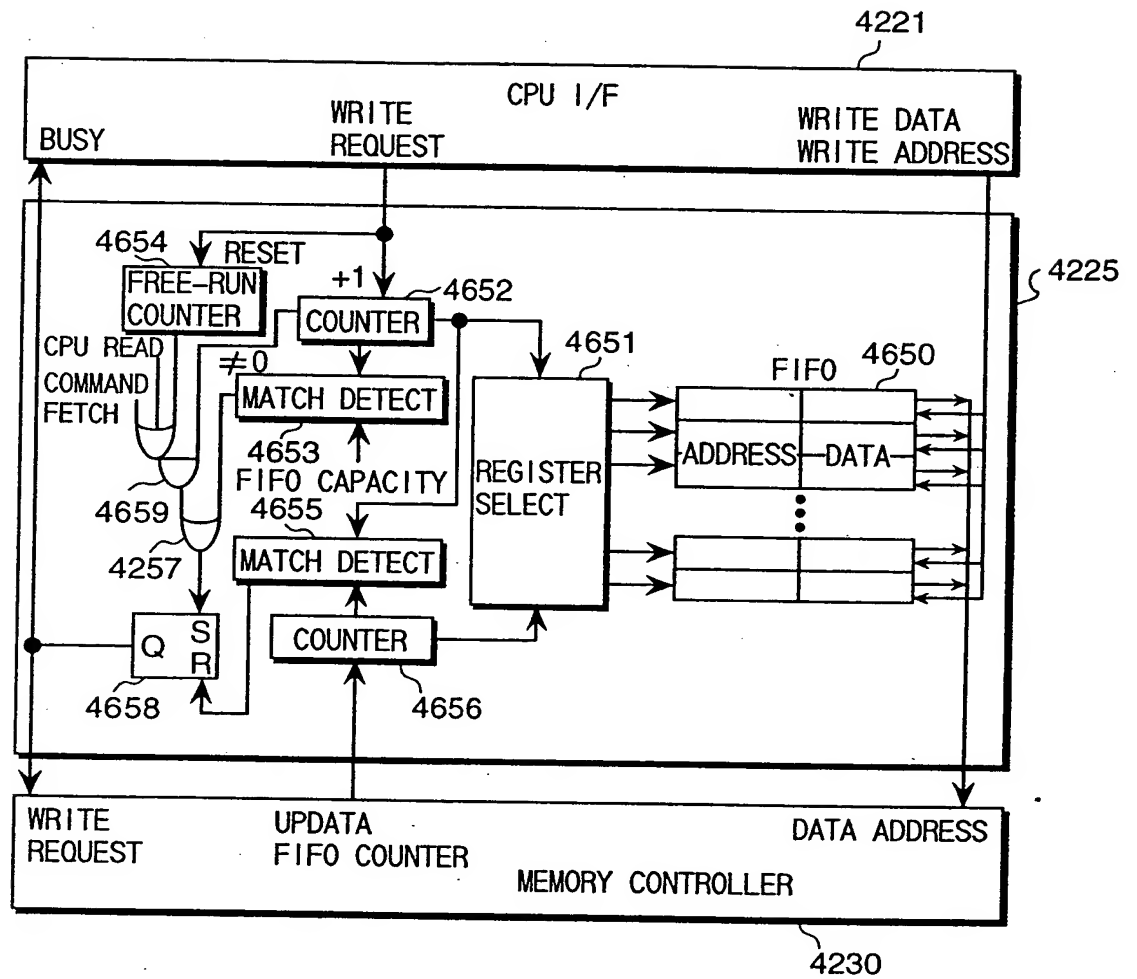


FIG. 47

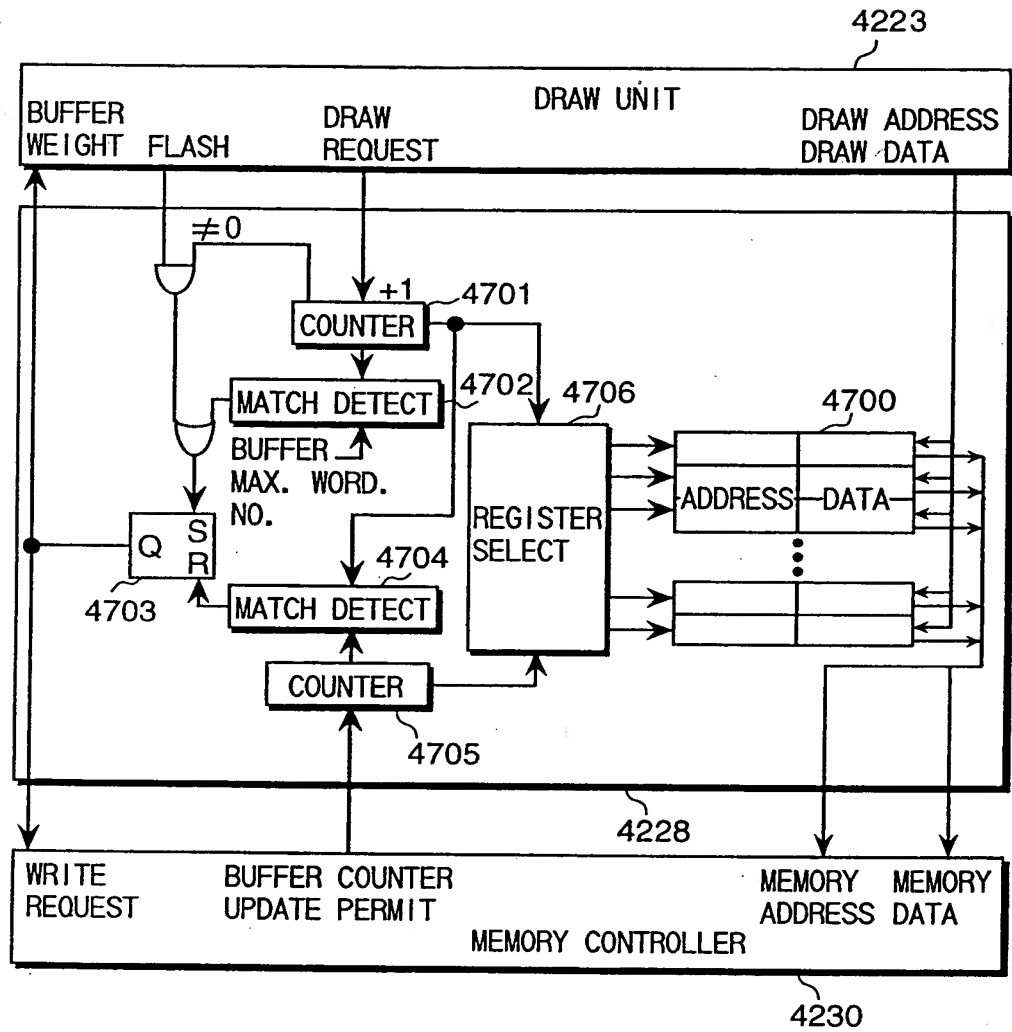


FIG.48

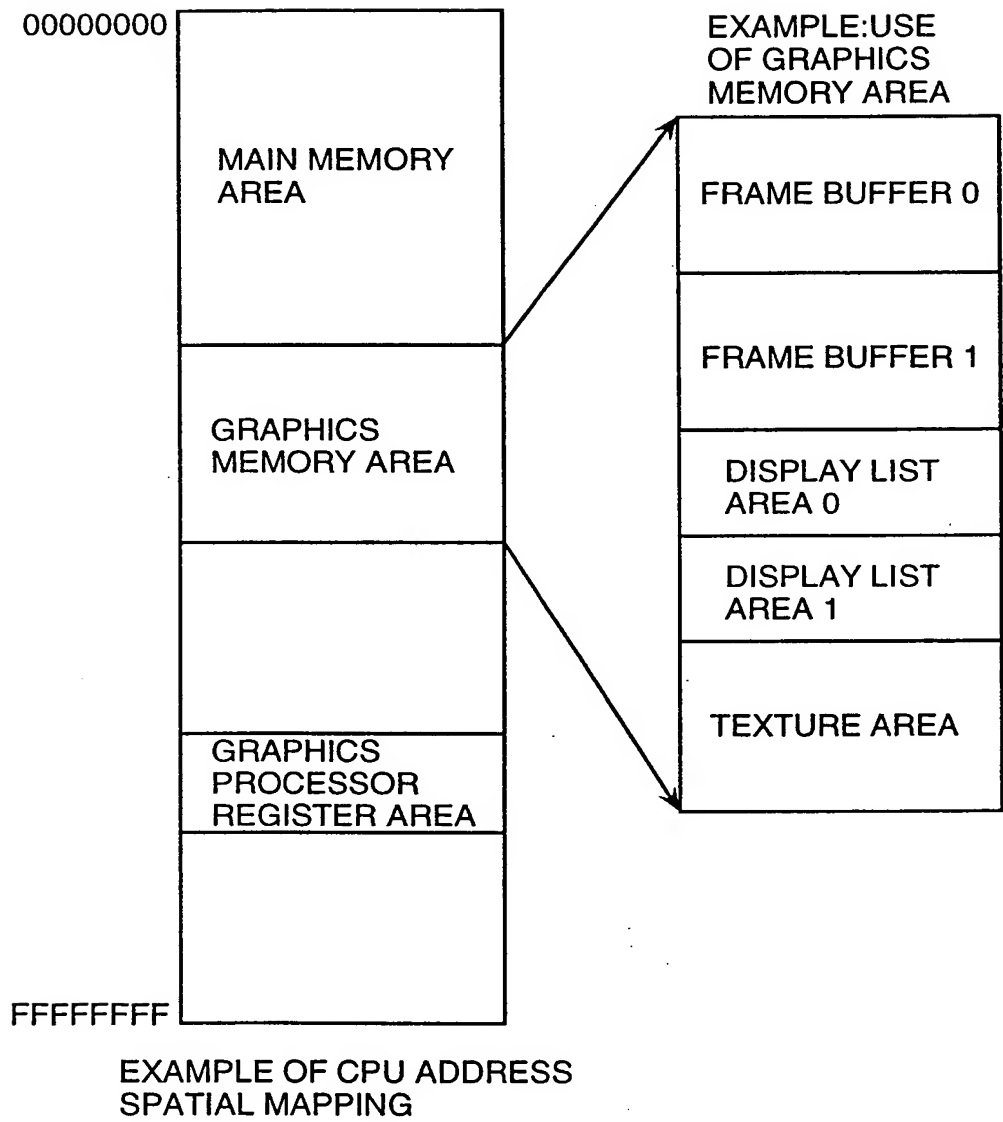


FIG.49

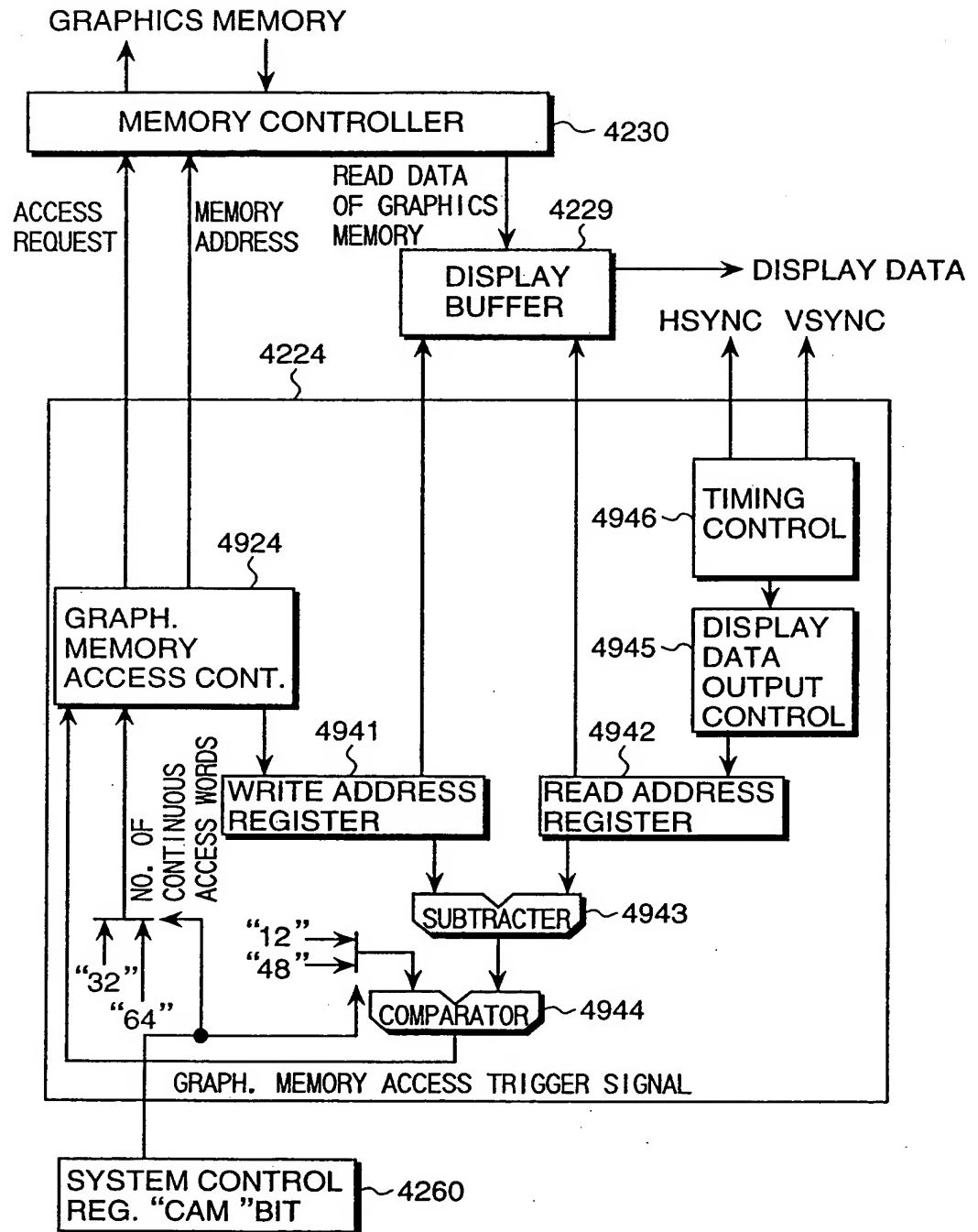
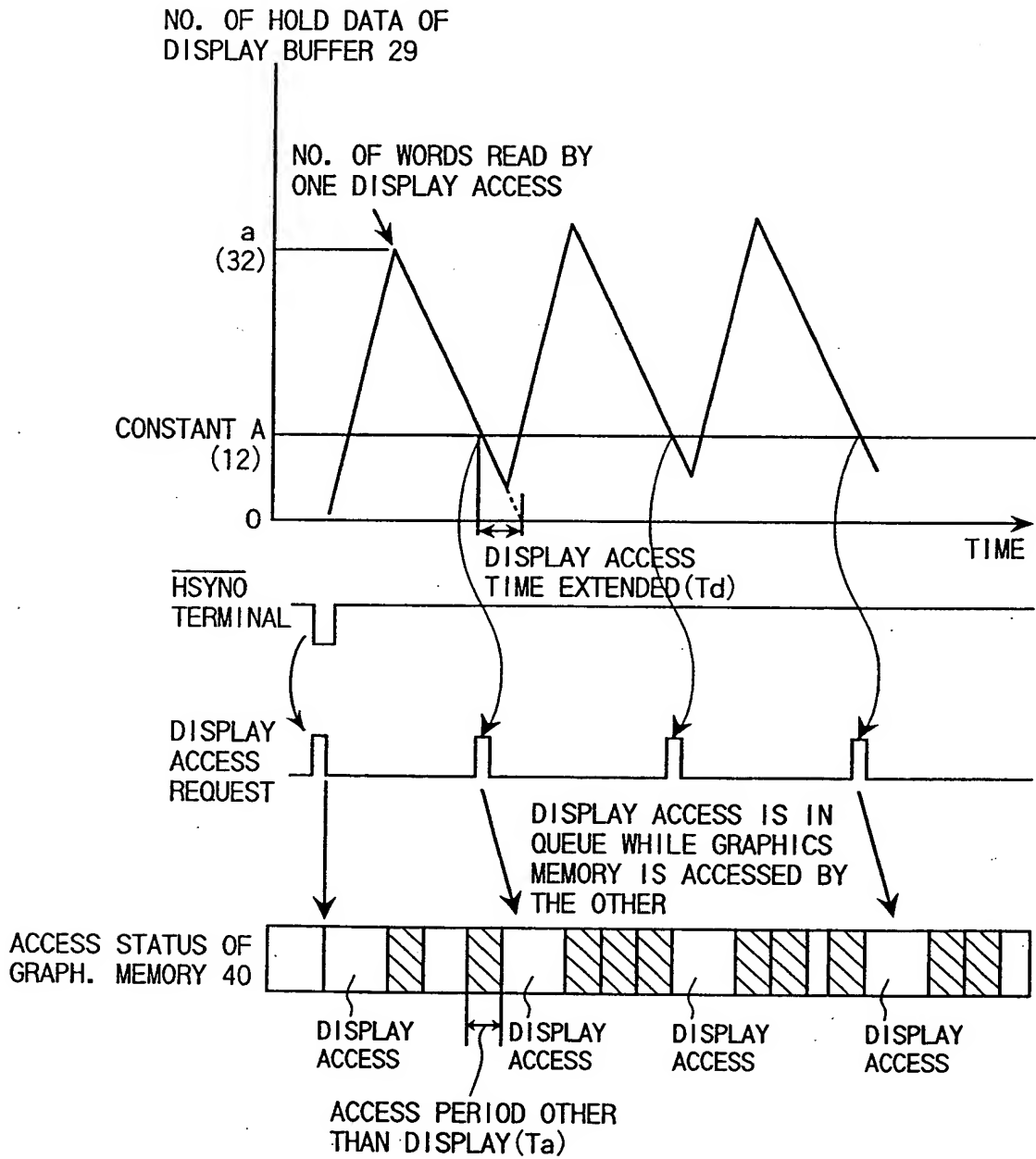


FIG. 50



▨ : INDICATES ACCESS OTHER THAN DISPLAY BEING EXECUTED

FIG.51

